

• $SIZE(T) :=$

Lecture 17

Set of languages L

decidable by an $O(T)$ -sized

circuit family

$$* \forall x, x \in L \leftrightarrow C_{|x|}(x) = 1$$

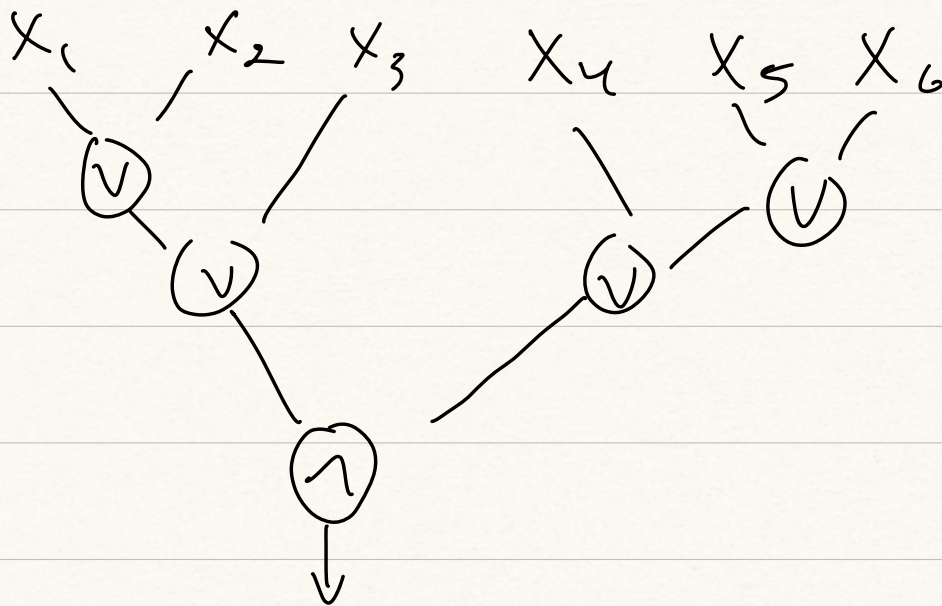
$$\{C_n\}_{n \in \mathbb{N}}$$

Facts:

(1) 3CNF is a special-case
of circuits

$$\phi = (x_1 \vee x_2 \vee x_3) \wedge$$

$$(x_4 \vee x_5 \vee x_6)$$



- Every $f: \{0,1\}^n \rightarrow \{0,1\}$ has $O(n2^n)$ sized CNF computing it
 $\Rightarrow O(n2^n)$ sized circuit

* Shannon: $O(2^n/n) \forall f$

* Others: $(2^n/n)(1+o(1))$

$$\underline{\text{Def.}}: P_{\text{poly}} = \bigcup_{c \in \mathbb{N}} \text{SIZE}(n^c)$$

Properties of P_{poly}

Thm: $P \subseteq P_{\text{poly}}$

Proof:

① Recall any time- T
TM M has an

equivalent oblivious TM

N running in time

$$O(T \log T) = O(T^2)$$

$$\bullet \forall x \quad M(x) = N(x)$$

and the positions of
 N 's tape heads are only
a function of $|x|$
and the current
time step i .

* For all inputs of length
 n , $N(x)$ does the
exact same thing

* Now, we argue every
time- T OTM has an
 $O(T)$ -sized circuit.

- Highlights

- $N(x)$ runs in time

$$t = T(|x|)$$

- Define transcript of $N(x)$:

$$z_1, z_2, \dots, z_t$$

$$\delta(z_i) = z_2 \dots \gamma(z_i) = z_{i+1}$$

↳ Each z_i is a snapshot

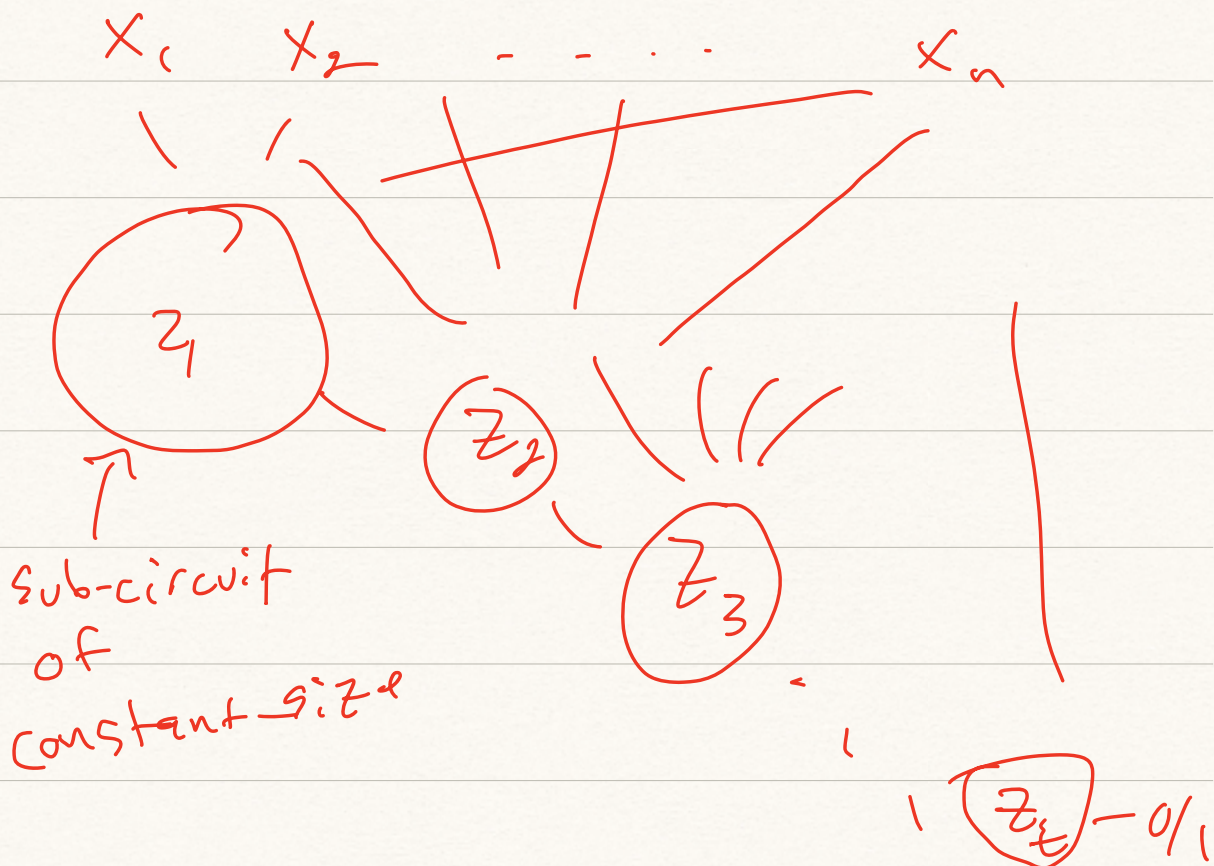
• N 's current state

- All k -symbols read by
 N 's tape heads

$$\Rightarrow |z_i| = O(1)$$

* Can compute each z_i
using a constant-size
circuit.

* Compose all these circuits
together, you get
 $O(t)$ -size circuit



* In the above proof, we
can do the transformation
in

- poly-time

- log-space *

* $P \neq P/poly$

• Because $\exists L \subset \{1\}^*$

$\subset \{1^n : n \in \mathbb{N}\}$

s.t. $L \in EXP$ or

L is undecidable

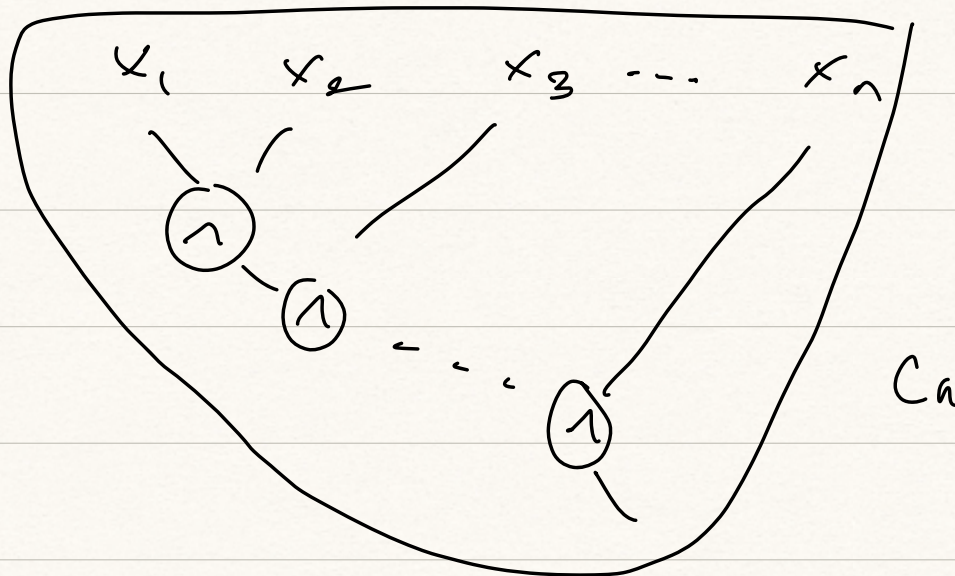
Lemma: $\forall L \subset \{I^n : n \in \mathbb{N}\}$

$L \in P/poly$

Proof:

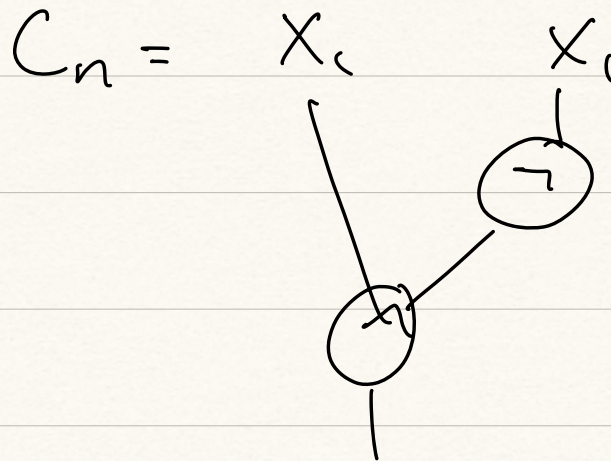
If $I^n \in L$,

C_n is the big AND
circuit



$$C_n(x) = 1 \iff x = I^n$$

If $I^n \notin L$, define



□

Ex:

$$UHALT := \left\{ 1^n \mid \begin{array}{l} \langle n \rangle_2 = (M, x) \\ \text{and} \\ \overline{\pi}_{TM}(x) \text{ halts} \end{array} \right\}$$

* $P/poly \not\subseteq EXP$
 $\not\subseteq NP$

$UHALT \in P/poly$

but not Turing Decidable.

Alternate Cook-Levin Thm/Proof

Def. CKT-SAT

$\equiv \{ C \mid C \text{ encodes}$
a circuit
with 1-bit
output and

$\exists w \text{ s.t.}$
 $C(w) = 1 \}$

Thm : CKT-SAT is
NP-complete.

① CKT-SAT $\in$ NP.

- Certificate is w

$$\text{s.t. } C(\omega) = 1$$

② CKT-SAT is NP-Hard

$$\forall L \in \text{NP}$$

$$L \leq_p \text{CKT-SAT}$$

$\Rightarrow$ Proof we did before

transforming def.

Verifier M_c to

circuit of size $O(T^2)$

if M_c runs in time T

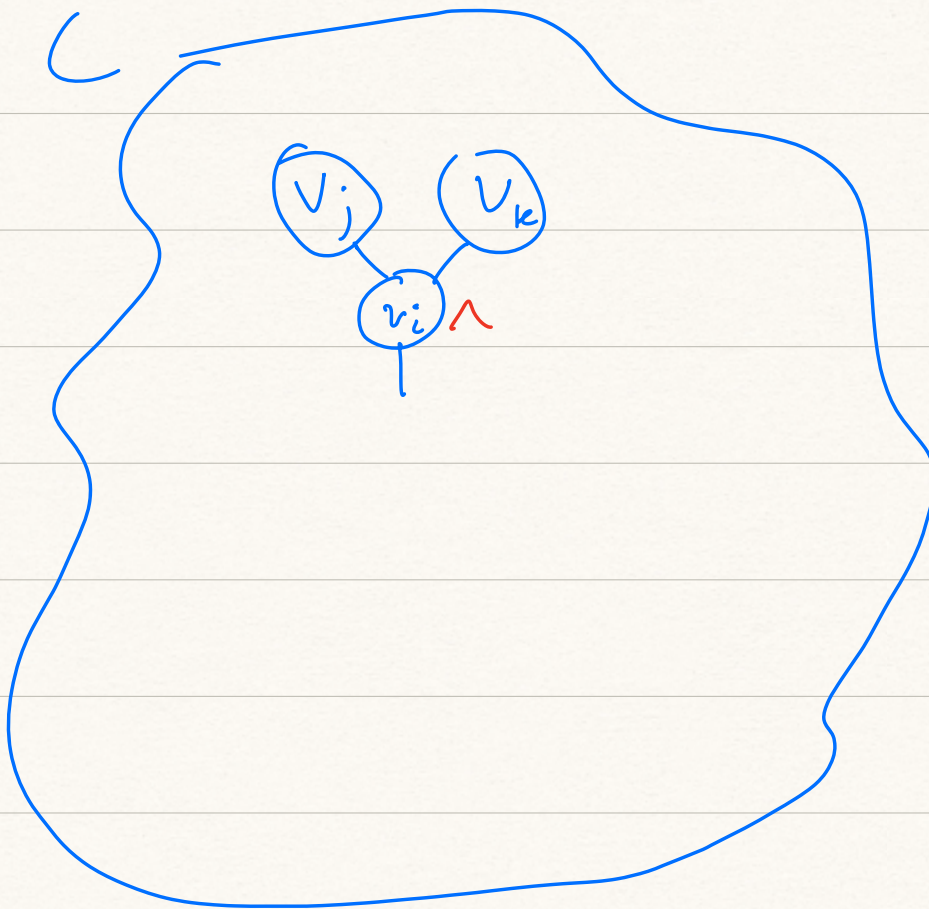
$$x \in L \iff \exists \omega \text{ s.t. } M_c(x, \omega) = 1$$



transform into C_x st.

$$C_x(w) = 1.$$

$$(3) \text{ CKT-SAT} \leq_p \text{3SAT}$$



$$\underline{(v_j \wedge v_k)} = v_i \quad \text{nodes in } c$$

↓

$$(z_j \wedge z_k) = z_i \mapsto \begin{array}{l} 4 \text{ clauses} \\ \text{in} \\ \exists \text{CNF} \end{array}$$

Boolean equality

$$X = Y \mapsto \underline{(X \vee \bar{Y}) \wedge (\bar{X} \vee Y)}$$

$$\underline{(v_j \vee v_k)} = v_i$$

$$(z_j \vee z_k) = z_i$$

$$\hookrightarrow (\bar{z}_i \vee z_j \vee z_k)$$

$$\wedge (z_i \vee \overline{z_j}) \wedge (z_i \vee \overline{z_k})$$

NOT
 $v_j \mapsto v_i$

$$\overline{z_j} = z_i \mapsto (z_i \vee \overline{z_j}) \wedge (\overline{z_i} \vee z_j)$$

Corollary: If $A \in \text{DTIME}(T)$
 then $A \in \text{SIZE}(T^2)$

Uniformly Generated Circuits

• If $L \in P/\text{poly}$, it's enough
 that $\{C_n\}$ exists.

Def: $\{C_n\}$ is P-uniform

if there exists a poly-time

f.m. M s.t. $\forall n$

$$M(1^n) = C_n$$

Thm: L has a P-uniform

circuit family iff $L \in P$.

Proof: $\forall x$: $n = |x|$

$\left\{ \begin{array}{l} x \in L \iff C_n(x) = 1 \\ \text{where } M(1^n) = C_n \end{array} \right.$
P-uniform $\underbrace{\text{poly}(n)}$

Define: $N(x)$:

- Run $M(1^x) = C_x$
- Output $C_x(x)$

clearly poly-time.

• Other direction

$$A \in \text{DTIME}(T)$$

$$\Rightarrow A \in \text{SIZE}(T^2) \quad \square$$

Logspace Uniform Circuits

↳ P-uniform circuits that
are (implicitly) logspace

$$M(1^n) = C_n \quad \text{using}$$

$O(\log n)$ space on
non-output tape

* $M(1^n)$ outputs i th
bit of $\langle C_n \rangle_2$ in
logspace for all i .

Thm: L is decidable by a
logspace uniform circuit
family iff $L \in P$.

P/poly vs. NP

Thm. ~~If~~ $NP \subseteq P/poly$, then
 $PH = \Sigma_2^P$.

Proof next time

* Show $\Pi_2^P \subseteq \Sigma_2^P$
if $NP \subseteq P/poly$ *

P/poly vs. EXP?

Thm: If $EXP \subseteq P/poly$
then $EXP = \Sigma_2^P$

BPP vs. P/poly?

* $P \subseteq BPP$

• Thm: $BPP \subseteq P/poly$.

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