Application heartbeats: a technique for enhancing system self-adaptability

BY

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THESIS

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Chicago, Illinois
To my family
ACKNOWLEDGMENTS

I seriously hate acknowledgments because they often end up doing more harm than good. Yet there are a lot of people that have made this thesis possible and that, in some way or another, have supported me during these years. I want to thank them because I am truly indebted to them.

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To my fiancée, Lorenza. I want to build something great with you.

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# TABLE OF CONTENTS

<table>
<thead>
<tr>
<th>CHAPTER</th>
<th>PAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Summary</td>
<td>xiv</td>
</tr>
<tr>
<td>1 INTRODUCTION</td>
<td>1</td>
</tr>
<tr>
<td>1.1 Introduction to the problem</td>
<td>1</td>
</tr>
<tr>
<td>1.1.1 Hardware adaptability</td>
<td>2</td>
</tr>
<tr>
<td>1.1.2 Software adaptability</td>
<td>3</td>
</tr>
<tr>
<td>1.2 Autonomic software systems</td>
<td>6</td>
</tr>
<tr>
<td>1.2.1 Terminology</td>
<td>6</td>
</tr>
<tr>
<td>1.2.2 Motivation</td>
<td>6</td>
</tr>
<tr>
<td>1.2.3 Definition and history</td>
<td>9</td>
</tr>
<tr>
<td>1.3 Involved disciplines</td>
<td>11</td>
</tr>
<tr>
<td>2 STATE OF THE ART</td>
<td>15</td>
</tr>
<tr>
<td>2.1 Characterizing autonomic computing systems</td>
<td>15</td>
</tr>
<tr>
<td>2.1.1 General concepts</td>
<td>15</td>
</tr>
<tr>
<td>2.1.2 Autonomic objects</td>
<td>17</td>
</tr>
<tr>
<td>2.1.3 Sensors</td>
<td>19</td>
</tr>
<tr>
<td>2.1.3.1 Design principles</td>
<td>19</td>
</tr>
<tr>
<td>2.1.3.2 Varieties of sensors</td>
<td>20</td>
</tr>
<tr>
<td>2.1.4 Actuators</td>
<td>21</td>
</tr>
<tr>
<td>2.1.5 Characteristics of adaptation</td>
<td>23</td>
</tr>
<tr>
<td>2.2 Monitoring and acting in autonomic systems</td>
<td>26</td>
</tr>
<tr>
<td>2.3 Instrumentation</td>
<td>27</td>
</tr>
<tr>
<td>2.3.1 Supported instrumentation</td>
<td>29</td>
</tr>
<tr>
<td>2.3.1.1 Tracing infrastructures</td>
<td>29</td>
</tr>
<tr>
<td>2.3.1.2 Assertions</td>
<td>31</td>
</tr>
<tr>
<td>2.3.2 Heartbeats</td>
<td>32</td>
</tr>
<tr>
<td>2.3.3 Forced instrumentation</td>
<td>33</td>
</tr>
<tr>
<td>2.4 Adaptation engine</td>
<td>37</td>
</tr>
<tr>
<td>3 PROPOSED METHODOLOGY</td>
<td>40</td>
</tr>
<tr>
<td>3.1 Our vision</td>
<td>40</td>
</tr>
<tr>
<td>3.1.1 Overview</td>
<td>40</td>
</tr>
<tr>
<td>3.1.2 A system based on observing, deciding, and acting</td>
<td>42</td>
</tr>
<tr>
<td>3.1.2.1 Observe</td>
<td>42</td>
</tr>
<tr>
<td>3.1.2.2 Decide</td>
<td>43</td>
</tr>
<tr>
<td>3.1.2.3 Act</td>
<td>44</td>
</tr>
<tr>
<td>3.2 Consensus Object: design and surrounding architecture</td>
<td>46</td>
</tr>
<tr>
<td>CHAPTER</td>
<td>PAGE</td>
</tr>
<tr>
<td>---------</td>
<td>------</td>
</tr>
<tr>
<td>3.2.1</td>
<td>The need for a Consensus Object</td>
</tr>
<tr>
<td>3.2.2</td>
<td>Terminology</td>
</tr>
<tr>
<td>3.2.3</td>
<td>Execution scenario</td>
</tr>
<tr>
<td>3.2.4</td>
<td>A non-centralized approach</td>
</tr>
<tr>
<td>3.2.5</td>
<td>A consideration about performance goals retrieval</td>
</tr>
<tr>
<td>3.2.6</td>
<td>Services</td>
</tr>
<tr>
<td>3.2.7</td>
<td>Communication infrastructure</td>
</tr>
<tr>
<td>3.2.7.1</td>
<td>Monitored applications information flows</td>
</tr>
<tr>
<td>3.2.7.2</td>
<td>Service registration and control interface</td>
</tr>
<tr>
<td>3.2.7.2.1</td>
<td>Service registration</td>
</tr>
<tr>
<td>3.2.7.2.2</td>
<td>Service interface</td>
</tr>
<tr>
<td>3.2.8</td>
<td>Autonomic libraries</td>
</tr>
<tr>
<td>3.2.8.1</td>
<td>Decision algorithm</td>
</tr>
<tr>
<td>3.2.9</td>
<td>Decision policies</td>
</tr>
<tr>
<td>4</td>
<td>PROPOSED IMPLEMENTATION</td>
</tr>
<tr>
<td>4.1</td>
<td>Implementation considerations</td>
</tr>
<tr>
<td>4.2</td>
<td>DES: an example of Implementations Library</td>
</tr>
<tr>
<td>4.2.1</td>
<td>General description</td>
</tr>
<tr>
<td>4.2.2</td>
<td>Implementation Switch Service</td>
</tr>
<tr>
<td>4.2.3</td>
<td>System overview</td>
</tr>
<tr>
<td>4.2.4</td>
<td>des-fpga code highlights</td>
</tr>
<tr>
<td>4.2.4.1</td>
<td>Help screen</td>
</tr>
<tr>
<td>4.2.4.2</td>
<td>Structure of the program</td>
</tr>
<tr>
<td>4.3</td>
<td>Services API</td>
</tr>
<tr>
<td>4.3.1</td>
<td>Functions</td>
</tr>
<tr>
<td>4.3.2</td>
<td>Service guidelines</td>
</tr>
<tr>
<td>4.3.3</td>
<td>Portability</td>
</tr>
<tr>
<td>4.4</td>
<td>Consensus Object</td>
</tr>
<tr>
<td>4.4.1</td>
<td>Functions</td>
</tr>
<tr>
<td>4.4.2</td>
<td>Consensus Object guidelines</td>
</tr>
<tr>
<td>4.4.3</td>
<td>Portability</td>
</tr>
<tr>
<td>5</td>
<td>IMPLEMENTATION AND EXPERIMENTAL RESULTS</td>
</tr>
<tr>
<td>5.1</td>
<td>Application Heartbeats overhead with des-fpga</td>
</tr>
<tr>
<td>5.1.1</td>
<td>Benchmarks on a FPGA-based system</td>
</tr>
<tr>
<td>5.1.2</td>
<td>Benchmarks on a x86-based system</td>
</tr>
<tr>
<td>5.2</td>
<td>Implementations Library: putting DES to test</td>
</tr>
<tr>
<td>5.2.1</td>
<td>Environment</td>
</tr>
<tr>
<td>5.2.2</td>
<td>Static analysis</td>
</tr>
<tr>
<td>5.2.3</td>
<td>A dynamic scenario</td>
</tr>
<tr>
<td>5.2.4</td>
<td>Consensus object and Services: tests</td>
</tr>
<tr>
<td>CHAPTER</td>
<td>PAGE</td>
</tr>
<tr>
<td>---------</td>
<td>------</td>
</tr>
<tr>
<td>5.2.4.1</td>
<td>Services tests</td>
</tr>
<tr>
<td>5.2.4.2</td>
<td>Consensus Object tests</td>
</tr>
<tr>
<td>5.2.4.3</td>
<td>Overhead on core allocator service</td>
</tr>
<tr>
<td>6</td>
<td>CONCLUSIONS AND FUTURE WORKS</td>
</tr>
<tr>
<td></td>
<td>CITED LITERATURE</td>
</tr>
<tr>
<td></td>
<td>VITA</td>
</tr>
</tbody>
</table>
# LIST OF TABLES

<table>
<thead>
<tr>
<th>TABLE</th>
<th>PAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>I</td>
<td>100</td>
</tr>
<tr>
<td>II</td>
<td>101</td>
</tr>
<tr>
<td>III</td>
<td>103</td>
</tr>
<tr>
<td>IV</td>
<td>104</td>
</tr>
</tbody>
</table>

- TABLE I: TABLE SHOWING A SAMPLE OF THE GATHERED DATA FOR THE FPGA-BASED EXAMPLE
- TABLE II: TABLE SHOWING THE AVERAGE EXECUTION TIME AND THE PERCENTAGE AVERAGE IMPACT ON IT CAUSED BY THE APPLICATION HEARTBEATS IN THE FPGA-BASED SCENARIO
- TABLE III: TABLE SHOWING A SAMPLE OF THE GATHERED DATA
- TABLE IV: TABLE SHOWING THE AVERAGE EXECUTION TIME AND THE PERCENTAGE AVERAGE IMPACT ON IT CAUSED BY THE APPLICATION HEARTBEATS IN THE X86-BASED SCENARIO
# LIST OF FIGURES

<table>
<thead>
<tr>
<th>FIGURE</th>
<th>DESCRIPTION</th>
<th>PAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>The evolution of software engineering</td>
<td>7</td>
</tr>
<tr>
<td>2</td>
<td>Self-* properties hierarchy</td>
<td>12</td>
</tr>
<tr>
<td>3</td>
<td>Self-adaptation loop</td>
<td>17</td>
</tr>
<tr>
<td>4</td>
<td>A different view: MAPE-K loop</td>
<td>18</td>
</tr>
<tr>
<td>5</td>
<td>Comparing internal and external adaptation</td>
<td>24</td>
</tr>
<tr>
<td>6</td>
<td>Heartbeat API, the two adaptation scenarios: (a) self-optimization of an application; (b) optimization of global parameters by an external observer in a setting with one or more applications.</td>
<td>34</td>
</tr>
<tr>
<td>7</td>
<td>Consensus object role: (1) dynamically update the list of available actuators and possible targets; (2) decide which actuator to enable on which target.</td>
<td>45</td>
</tr>
<tr>
<td>8</td>
<td>General concept behind informations flows F1, F2, F3, F4, and F6 in a multi-service and multi-application scenario</td>
<td>55</td>
</tr>
<tr>
<td>9</td>
<td>Service registration and service interface</td>
<td>59</td>
</tr>
<tr>
<td>10</td>
<td>(1) Implementations library; (2) Parameters library</td>
<td>61</td>
</tr>
<tr>
<td>11</td>
<td>(1) Normal operation with object L1 (that is referenced in the Object Translation Table, OTT). (2) L1 has to be replaced by an improved or optimized version called L2. First of all L2 is created, L1 reaches a safe state, and then L1’s state is transferred to L2. (3) References are updated and L1 is deleted.</td>
<td>65</td>
</tr>
<tr>
<td>12</td>
<td>Global view of the information flows. The picture shows potential ways to deliver the flows defined in Section 3.2.7</td>
<td>66</td>
</tr>
<tr>
<td>13</td>
<td>The API that gathers decision policies</td>
<td>70</td>
</tr>
<tr>
<td>14</td>
<td>Self-Aware Adaptive computing system</td>
<td>74</td>
</tr>
<tr>
<td>FIGURE</td>
<td>Description</td>
<td>PAGE</td>
</tr>
<tr>
<td>--------</td>
<td>----------------------------------------------------------------------------------------------------------------------------------------------------------------</td>
<td>------</td>
</tr>
<tr>
<td>15</td>
<td>Hot-Swap mechanism</td>
<td>75</td>
</tr>
<tr>
<td>16</td>
<td>Application Heartbeats API overhead in the FPGA-based scenario</td>
<td>102</td>
</tr>
<tr>
<td>17</td>
<td>Application Heartbeats API overhead in the x86-based scenario</td>
<td>105</td>
</tr>
<tr>
<td>18</td>
<td>FPGA architecture</td>
<td>107</td>
</tr>
<tr>
<td>19</td>
<td>Execution times</td>
<td>108</td>
</tr>
<tr>
<td>20</td>
<td>Application execution behavior. The interval between $m$ and $M$ defines the desired heart rate window</td>
<td>110</td>
</tr>
<tr>
<td>21</td>
<td>Service initialization. Ten executions, average is in red.</td>
<td>112</td>
</tr>
<tr>
<td>22</td>
<td>Service registration. Ten executions, average is in red.</td>
<td>113</td>
</tr>
<tr>
<td>23</td>
<td>Reading a command. Ten executions, average is in red.</td>
<td>114</td>
</tr>
<tr>
<td>24</td>
<td>Process registration. Ten executions, average is in red.</td>
<td>115</td>
</tr>
<tr>
<td>25</td>
<td>Writing a command. Ten executions, average is in red.</td>
<td>116</td>
</tr>
<tr>
<td>26</td>
<td>Overhead of the centralized autonomic infrastructure at service startup. Average values are shown at the top.</td>
<td>117</td>
</tr>
<tr>
<td>27</td>
<td>Service</td>
<td>118</td>
</tr>
<tr>
<td>28</td>
<td>Core allocator: overhead of the centralized autonomic infrastructure. Average values are shown at the top.</td>
<td>119</td>
</tr>
</tbody>
</table>
# LIST OF ABBREVIATIONS

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>AH</td>
<td>Application Heartbeats</td>
</tr>
<tr>
<td>AI</td>
<td>Artificial Intelligence</td>
</tr>
<tr>
<td>API</td>
<td>Application Programming Interface</td>
</tr>
<tr>
<td>CBSE</td>
<td>Component-Based Software Engineering</td>
</tr>
<tr>
<td>CSAIL</td>
<td>Computer Science and Artificial Intelligence Laboratory</td>
</tr>
<tr>
<td>CO</td>
<td>Consensus object</td>
</tr>
<tr>
<td>CPU</td>
<td>Central Processing Unit</td>
</tr>
<tr>
<td>DES</td>
<td>Data Encryption Standard</td>
</tr>
<tr>
<td>FPGA</td>
<td>Field Programmable Gate Array</td>
</tr>
<tr>
<td>GCC</td>
<td>GNU Compiler Collection</td>
</tr>
<tr>
<td>GNU</td>
<td>GNU is Not Unix</td>
</tr>
<tr>
<td>GPP</td>
<td>General Purpose Processor</td>
</tr>
<tr>
<td>HB</td>
<td>Heartbeats</td>
</tr>
<tr>
<td>HDL</td>
<td>Hardware Description Language</td>
</tr>
<tr>
<td>I/O</td>
<td>Input/Output</td>
</tr>
<tr>
<td>ICAP</td>
<td>Internal Configuration Access Port</td>
</tr>
<tr>
<td>Abbreviation</td>
<td>Definition</td>
</tr>
<tr>
<td>--------------</td>
<td>------------</td>
</tr>
<tr>
<td>IPC</td>
<td>Inter-Process Communication</td>
</tr>
<tr>
<td>IPCM</td>
<td>IP-Core Manager</td>
</tr>
<tr>
<td>IP-Core</td>
<td>Intellectual Property Core</td>
</tr>
<tr>
<td>ISS</td>
<td>Implementation Switch Service</td>
</tr>
<tr>
<td>JIT</td>
<td>Just In Time</td>
</tr>
<tr>
<td>LTT</td>
<td>Linux Trace Toolkit</td>
</tr>
<tr>
<td>MAPE-K</td>
<td>Monitor, Analyze, Plan, Execute through Knowledge</td>
</tr>
<tr>
<td>MPMC</td>
<td>Multi-Port Memory Controller</td>
</tr>
<tr>
<td>MIT</td>
<td>Massachusetts Institute of Technology</td>
</tr>
<tr>
<td>ODA</td>
<td>Observe Decide Act</td>
</tr>
<tr>
<td>OPB</td>
<td>On-chip Peripheral Bus</td>
</tr>
<tr>
<td>OTT</td>
<td>Object Translation Table</td>
</tr>
<tr>
<td>PA</td>
<td>Performance Assertions</td>
</tr>
<tr>
<td>PCS</td>
<td>Performance Counter Subsystem</td>
</tr>
<tr>
<td>PID</td>
<td>Process Identifier</td>
</tr>
<tr>
<td>PLB</td>
<td>Processor Local Bus</td>
</tr>
<tr>
<td>POLIMI</td>
<td>Politecnico di Milano</td>
</tr>
<tr>
<td>QoS</td>
<td>Quality of Service</td>
</tr>
</tbody>
</table>
LIST OF ABBREVIATIONS (Continued)

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Full Form</th>
</tr>
</thead>
<tbody>
<tr>
<td>SoA</td>
<td>State of the Art</td>
</tr>
<tr>
<td>SOA</td>
<td>Service Oriented Architecture</td>
</tr>
<tr>
<td>SoC</td>
<td>System-on-Chip</td>
</tr>
<tr>
<td>TID</td>
<td>Thread Identifier</td>
</tr>
<tr>
<td>UIC</td>
<td>University of Illinois at Chicago</td>
</tr>
<tr>
<td>VHDL</td>
<td>VHSIC Hardware Description Language</td>
</tr>
<tr>
<td>XUPV2P</td>
<td>Xilinx University Program Virtex-II Pro</td>
</tr>
</tbody>
</table>
SUMMARY

Nowadays the complexity of computing systems is skyrocketing. Programmers have to deal with extremely powerful computing systems that take time and considerable skills to be instructed to perform at their best. It is clear that it is not feasible to rely on human intervention to tune a system: conditions change constantly, rapidly, and unpredictably. It would be desirable to have the system adapt to the mutating environment.

This dissertation aims at providing the architecture that makes it possible for a computing system to observe its current status and its current performance, to decide which modifications to undertake, and to act to modify its behavior. The final aim is to be able to build systems capable of reacting to unfavorable, unpredictable, and unknown situations. Such systems are known as self-adaptive or autonomic systems. They also are an example of goal-orientation because they know their objectives but they might not know how to reach them or they might change the how depending on the context. In other words their input is not how but what.

In this work the complete architecture to build such types of systems is presented. Starting from a simple yet powerful monitoring facility, we develop: the central entity that gathers all the information about system performance and that is able to decide which measures to enact; the communication channels through which the decisions are conveyed; the elements that commit the changes.

In particular we make use of an extremely lightweight performance monitor developed at the Massachusetts Institute of Technology (MIT) Computer Science and Artificial Intelligence Lab-
oratory (CSAIL), called Application Heartbeat (67; 68) and that has been developed in the past two years.¹

The novel contribution of this thesis is to provide an infrastructure to build autonomic systems that is:

• complete, since it monitors, decides, and enacts

• simple yet capable of being used in complex scenarios

• lightweight, as demonstrated in Chapter 5, since it has a low overhead both on the monitored applications and on the handled actuators.

Such characteristics, along with the encouraging experimental results, prove that the proposed solution has the potential to be expanded and move from an enabling technology to a working product.

This remainder of dissertation is organized as follows.

Chapter 1 describes the research context and provides an introduction to the basic concepts involved in autonomic computing.

¹My work has also some blander links with other MIT CSAIL project such as fos (Factored Operating System), Smartlocks (93), and Code Perforation techniques (44). All of these will be discussed in more detail in Chapter 2 and Chapter 3. In this dissertation I will explicitly disambiguate the work carried out by the Massachusetts Institute of Technology group and my individual work. In particular I will use the terms “we”, “our”, “I”, and “my” to refer to the work I have done under the supervision of my advisor, Professor Santambrogio. I will explicitly attribute work done by MIT CSAIL group.
Chapter 2 reviews related researches and describes the State-of-the-Art in self-adaptive systems. The *Observe- Decide-Act* loop, sensors, instrumentation, and sensors are explained in detail.

Chapter 3 describes in detail the proposed solution. In particular, the relation with the *Observe- Decide-Act* loop, the decision engine (*Consensus Object*), and the actuators communication interface (*Services API*) are described.

Chapter 4 presents in great detail how the *Consensus Object*, the *Services API*, and a self-adaptive library have been implemented. Moreover guidelines for developers that want to extend the implementation are outlined.

Chapter 5 experimentally evaluates the implementation of the *Consensus Object*, the *Services API*, and the self-adaptive library. The overhead of the implemented self-adaptive architecture is quantified.

Chapter 6 concludes with a final discussion and outlines future works.
CHAPTER 1

INTRODUCTION

“...Bond. James Bond.”
Dr. No (1962)

This Chapter provides an introduction to the context in which this dissertation is settled and to the basic concepts that will be referred to throughout all the other Chapters.

1.1 Introduction to the problem

Nowadays computer systems are rapidly evolving and are becoming increasingly complex. Complexity is upturning difficulties in keeping a system maintainable. In fact, even before a certain product is launched, some issues, such as bugs and security issues, might already be known. The system has then to be updated in a fast cost-efficient manner. This drastically shortens the life-cycle of products and makes both hardware and software subject to an unprecedented rate of change. There is a strong need to make future computer systems not only easily upgradeable but also able to:

• reduce (ideally to zero) down-time caused by updates. At present down-time is in fact reducing dependability and availability of systems. Some steps in this direction are represented by the work done in K42 (1).
be proactive and suggestive towards changes. The stimulus for an improvement or optimization should not only be driven by an external entity but might be advocated by the system itself. This is a ferment field of research that will be discussed in Section 2.

The problem has been approached from two different perspectives: hardware and software adaptability. As we will see, these two approaches are usually considered unrelated. Section ?? will show a novel fusion of the two.

1.1.1 Hardware adaptability

Hardware adaptability has been actively researched since the introduction of Field Programmable Gate Arrays (FPGA) at the beginning of the 1990’s (2). FPGAs are usually part of hardware-software co-systems where the computationally critical parts (also called kernels (3)) are implemented in hardware to boost performance while the rest (usually controller-related tasks) is implemented in software. The most studied and probably most innovative feature of FPGAs is dynamic reconfigurability (4; 5): FPGAs are configured with one or more configuration files called bitstreams that describe a certain circuit to be implemented on the board.

While a complete reconfiguration usually interfere with functionality (for this reason the service provided by the FPGA is suspended), it is also possible to only partially reconfigure the board. Partial Dynamic Reconfiguration (PDR) enables reconfiguration of a part of the device at runtime without disrupt of functionality (6).

As outlined in (3) there is now a shift from reconfigurable architectures to autonomic systems capable of configuring, healing, optimizing, and protecting themselves. An instance of FPGA-
based online adaptive system has been described in (7) where PDR is used to change the architectural implementation with the aim of optimizing power consumption.

Although interesting, powerful, and extremely fast compared to software solutions, FPGAs have not become as widely adopted as expected. Cases of FPGA-based autonomic computing remain rare and it is contended that radically different architectures will soon be available and predominant: multi-cores are already extensively embraced and some expect silicons with thousands of cores to make their appearance in the next ten years (8).

FPGAs are not the only examples of adaptable hardware. Current architectures offer several other mechanisms that alter the status of the hardware and, consequently, performance. For instance, frequency scaling (9) is a technique that reduces processor clock by some multiple of the maximum (10) with the aim of making the CPU consume less power (at the expense of performance). Clock throttling is a technique that does not alter the frequency yet it gates the clock signal for some number of cycles at regular intervals, slowing down the computation. While frequency scaling does not require a change in voltage, dynamic voltage scaling (DVS) (11) lowers power consumption by lowering both the operating voltage and (proportionally) frequency.

1.1.2 Software adaptability

Software is a key element of modern computing systems: it drives the hardware and enables exploitation of its full power. The complexity of software is growing exponentially (12): programs are often developed by several programmers, built of several inter-communicating components, and run in unknown and mostly unpredictable contexts. Even with modern test-
ing techniques, this scenario often leads to software being deployed with relevant issues. Bugs and security issues have to (or should be) patched in a fast cost-efficient manner through update mechanisms built in the shipped software. But this is only part of the whole problem: in fact, not only software has to be polished and perfected after installation but it has to be designed in order to accommodate needs and fulfill requirements that become evident only time after deployment.

In order to understand this aspect it is useful to review the evolution of software design. 

*Software engineering* was born in the late 1960's when the study of a series of techniques to approach software design, development, and maintenance arose (13). The goal was desirable: being able to improve the quality of products building them in a more systematic and disciplined way. For the first time a solid alternative to the “code-and-fix” approach (Figure 1.a) was proposed: creating software was not synonym for programming anymore. In particular software started to be developed through the following phases (also shown in Figure 1.b):

- **Definition of stable requirements.** It starts with the analysis of the context in which the product will be used, the users that will make use of it, and the needs to be fulfilled.

- **Software design.** It is usually driven by tools such as the Unified Modeling Language (UML) and it is used to architect the software before it is implemented: the purpose it to make the software maintainable, modular, expandable, and reusable.

- **Software implementation.** It is the final step in engineering a software and it is based on the given design. It usually consists of programming and testing.
It was indeed argued that a conscientious definition of requirements could avoid radical changes in response to the demand for possibly simple new features: this clearly was a big step towards better quality.

Yet, after just a few years, this approach became not satisfactory because it was based on the arguable assumption that such thing as “stable requirements” existed (14). When software products became something more than static, monolithic, and centralized tools internal to organizations the so-called closed world assumption (14) did not hold anymore. Evolution is indeed intrinsic to software (15) and for this reason software engineers must design programs that can easily accommodate future changes. As outlined by (16) programs must be designed for change.

If in the previous years one single change could have implied recompilation and redeployment of the whole system, changes gradually became easier to introduce, also thanks to object-oriented programming languages. Software became modular, dynamic and distributed (also across networks). This has been shown in Figure 1.c.

Yet the growth in the dynamism of change has not shown signs of stopping any time soon. Even though most softwares developed in the past few years are easily updatable and have little downtime, new application and new domains demand more flexibility and performance. For instance, VisaNet, a network that handles credit card payments, is updated an average of 20000 times a year yet it is always above 99.5% availability (17).

Software still requires a lot of (costly) manual intervention, it sometimes still fails, and it has almost no knowledge about itself: the problem lays in the open-loop structure (18) followed in
the engineering of the software. There is a wide consensus on the fact that new paradigms have to be explored and new frameworks have to be developed. Among those, autonomic computing systems seem to be the response to most of the problems previously described (18). They introduce a new framework based on the closed-loop approach, which, through a feedback loop, adjusts the software while it is running (Figure 1.d). This kind of system needs to monitor itself and its context, discern significant changes, determine how to react, and execute decisions. The life-cycle of self-adaptive softwares does not stop after its development and initial setup but it indeed continues to handle changes on several possible levels.

The evolution of software engineering as described in this section is delineated in Figure 1.

1.2 Autonomic software systems

1.2.1 Terminology

Inspired by the lexicon embraced in (19) and by the willingness of not introducing useless complexity in the adopted vocabulary, in this dissertation I will interchangeably use the terms “autonomic”, “self-adaptive”, “self-managing”, and “organic”.

1.2.2 Motivation

As briefly described in Section 1.1.2, the primary motivation for autonomic computing is rooted in the fact that the costs of handling the complexity of software systems to achieve their goals is drastically increasing (20). In particular, traditional software has to face great challenges such as:
Figure 1. The evolution of software engineering
• complexity of management and maintenance: manual operations are still required to supervise, update, fix, ... a system.

• changing priorities and policies governing the goals

• resistance to unexpected conditions at runtime, which include:
  
  – hardware failures
  
  – software failures (such as severe bugs, security breaches, etc, ...)
  
  – contexts and conditions that are unknown to the software because they have not been considered in their design and implementation

The long-term goal is not only to build a system that does not have to be stopped in order to be updated but also to make it autonomous and intelligent enough to be able to trigger by itself the best possible changes.

We contend that software self-adaptability is becoming an important topic of research and is gradually earning relevance. As delineated in Chapter 2 there are many instances and contexts where self-adaptability is being investigated and implemented. In particular autonomic computing is becoming increasingly common in large-scale systems. We envision similar characteristics spread also among less comprehensive systems, such as consumer software.

We strongly believe that augmenting software with organic attribute will lead to a new era of computing.
1.2.3 Definition and history

Several definition of self-adaptive software have been provided through the years. Among the firsts:

- “Self-adaptive software evaluates its own behavior and changes behavior when the evaluation indicates that it is not accomplishing what the software is intended to do, or when better functionality or performance is possible” (21)

- “Self-adaptive software modifies its own behavior in response to changes in its operating environment. By operating environment, we mean anything observable by the software system, such as end-user input, external hardware devices and sensors, or program instrumentation” (22)

But autonomic computing was well formalized only in 2001 by IBM with its “Autonomic Computing Manifesto” (12), first presented by Dr. Paul Horn’s during the National Academy of Engineering meeting of the same year. Horn outlined eight key properties (“elements”) of autonomic computing systems:

1. **Self-Awareness or Introspection** — the system must know itself in detail: components, layers, statuses, connections, extents,…are necessary parts to be monitored and kept under inspection.

2. **Self-Reconfigurability** — the system must be able to dynamically change its behavior at runtime.
3. **Continuous Self-Optimization** — the system must never settle but it must continuously monitor itself to discover possible optimizations.

4. **Self-Healing Capabilities** — in case of failures (caused by routine or extraordinary events) the system must be able to recover.

5. **Self-Protection Techniques** — the system must detect, identify and protect itself against various types of attacks to maintain overall system security and integrity.

6. **Context-Awareness** — the system must know the environment (defined as: everything in the operating environment that affects the systems properties (18)) and the context that surround its activity and take it into consideration when acting.

7. **Openness** — the system must be able to interact with heterogeneous components that might not be under its control.

8. **Anticipation and Support** — the system must be able to provide and anticipate the correct status to handle future changes.

This “flat” description has evolved in the discussion provided by (18), which proposes a hierarchical view of the so-called self-* properties (see also Figure 2). Three levels are defined:

**General level** This level contains global properties of self-adaptive software. Self-adaptiveness is a very general term that includes self-managing, self-governing, self-maintenance, self-control, self-evaluating, and self-organizing.

**Major level** These properties have been defined in accordance to biological self-adaptation mechanisms. For this reason autonomic systems are often said “organic”. An “organism”
with such properties has to adapt to the context (just like the human body does, for instance in reaction to a temperature change) or the system itself. Those are the properties of this level in detail, which have been described previously:

- Self-reconfiguring
- Self-healing
- Self-optimizing
- Self-protecting

**Primitive level** This level features the most basic properties onto which all the others build on.

- Self-awareness
- Context-awareness
- (optional) Openness and anticipation

Autonomic computing is now researched by several groups around the world and has also witnessed considerable success in the commercial realm (23; 24; 25; 26).

### 1.3 Involved disciplines

As highlighted by (18), autonomic computing is intrinsically *interdisciplinary*. It does not belong to any particular field but it is indeed cross-cutting research.

Some of the involved areas are:
Figure 2. Self-* properties hierarchy
• **Software Engineering**, which offers several concepts that can be used in self-adaptive computing, such as: indicators to measure the *quality* of the software, requirements engineering (formal methods might be used to define and properties the system must satisfy), *Component-Based Software Engineering* (CBSE) might be applied to obtain modularity and reusability, *Service-Oriented Architecture* (SOA) might be employed to help the composition of loosely coupled service

• **Artificial Intelligence** might be applied to make self-adaptive softwares flexible and able to learn from the experience. It might make software able to recognize patterns and boost its ability to decide applying planning, reasoning, and learning techniques. For instance, goal-oriented requirements might be achieved through the use of *agent* and *multi-agent systems*; *Machine Learning* and *Soft Computing* might lend the concept of the “achieving” approach, which learns the best way to act analyzing the stream of sensed data; *Reinforcement Learning* has already been used for dynamic action selection (27) and decentralized collaborative self-adaptive software (28); moreover *decision theory* and *decision theoretic planning* (such as the Markov Decision Process and Bayesian Networks) can enhance the decision process.

• **Control Theory** lends the important notion of “feedback loop” that is the base for the sense-plan-act loop described in section 2.1.1.

• **Network and Distributed Computing** can contribute to self-adaptive computing with the concepts and theory developed in highly-scattered systems. *P2P networks, policy-based management* and *quality of service (QoS) management, virtualization, heartbeat* and *pulse*
monitoring (29) are interesting ideas that might be reused in the context of autonomic computing.

- *Computer architecture* is particularly involved when considering hardware-based solution for adaptivity.
In this chapter we analyze the way autonomic computing systems are in general implemented and what work has been carried out in the last few years in this field. Many researched have developed interesting projects that have some similarities with the work that will be presented in the next chapters.

2.1 Characterizing autonomic computing systems

2.1.1 General concepts

In 1.1.2 we have introduced the concept of closed-loop approach, meaning that autonomic software executes and, at the same time, monitors itself and the surrounding environment. This technique is used for multiple reasons: for example, a certain condition might be checked periodically and, if met, a certain action might be triggered; or maybe a certain optimization has been introduced and the systems want to inspect whether it is fulfilling the expectations.

In general we can say that there are certain elements that are intrinsic to autonomic computing systems:
**Sensors** They are the elements that gather the certain information about the system and the environment. They usually transmit it (i.e.: to a buffer, over the network, etc...) and, before doing that, they sometimes also elaborate it.

**Monitoring process** It gathers information from different sensors. It might preprocess the collected data in order to delineate patterns and discover indicators.

**Detecting process** It analyzes what has been gathered by the monitor and discovers conditions, problems, etc... It basically determines *when* and *where* a response is required.

**Deciding process** Given the results provided by the detector it chooses an action to perform (it might stay idle). It basically determines *what* to do and *how* to act.

**Acting process** Given the context it picks the most appropriate actuator. It is in charge of translating actions into tasks for the actuators.

**Actuators** They are the components that actually perform a certain change on the system.

These components form the loop shown in Figure 3. This description is very general and it fits the case of a *centralized* implementation. A *distributed* implementation has the same components but a different setup that poses an incredibly vast and interesting research topic: reaching a reasonably near optimal resolution in absence of a central entity. It is beyond the scope of this dissertation to discuss and describe in detail the distributed case, which is left as future work.

The loop in Figure 3 is usually called *adaptation loop* (18). There are several other names that can be used instead: *MAPE-K loop* (30) (shown in Figure 4), which stands for Monitoring,
Analyzing, Planning and Executing functions, that works thanks to a shared Knowledge-base; or Observe-Decide-Act (ODA) loop. The three definitions (adaptation loop, MAPE-K, and ODA) will be considered equivalent.

2.1.2 Autonomic objects

This discussion leads to the concept of autonomic object, as expressed in (31). This notion unquestionably helps the modularization of an autonomic computing system. An autonomic object exports at least four interfaces:
Figure 4. A different view: MAPE-K loop
1. a *control interface*, which exposes sensors and actuators. It allows external entities to control and monitor the object.

2. an *access interface*, which regulates access to sensors and actuators in order to enforce security. Users are assigned certain privileges depending on the state of the object and on the role of users.

3. a *rule interface*, which makes it possible to add, remove, or edit rules. A rule consists of a condition, an action (defined in terms of *control interface*, e.g.: sensors and actuators) to be performed in case the condition is fulfilled, and an action to be performed in case the condition is not fulfilled.

4. one or more *functional interfaces*

### 2.1.3 Sensors

#### 2.1.3.1 Design principles

Sensors always perturb (sometimes only slightly) the system that they help to monitor (32). For this reason an important principle is to *minimize the measurement overhead*. Some other rules to keep in mind are:

- Sensors should be lightweight
- Sensors should be accessible (potentially remotely accessible)
- Sensors should be reusable
2.1.3.2 Varieties of sensors

As outlined in many researches (33; 34) there are several types of sensors:

**sensors** They are small, efficient pieces of code residing within the routine being monitored (35).

There are different kinds of sensors:

**tracing sensors** They are usually implemented with one piece of code that reside directly in the monitored application. They report every single occurrence of an event within a certain interval of time. This usually happens synchronously with the occurrence of the event. Tracing sensors are used when all occurrences of an event must be known or when actions must be triggered in real-time following the manifestation of a certain event.

**sampling sensors** They are usually implemented with small piece of code that reside directly in the monitored application. They collect information at the request of the monitor. This usually happens asynchronously with the occurrence of the event. Sampling is used when knowing all the occurrences of an event and reacting immediately to a manifestation are not necessary properties.

**extended sensors** These kind of sensors augment tracing or sampling sensors with the capability of preprocessing, analyzing, and computing results. They usually are used in less centralized scenarios where the computation is spread and parallelized in order to reduce the burden on the central monitor.
**Probes** Probes slightly differ from all kinds of sensors that have been presented. They consist of two small, efficient pieces of code: one resides within the monitor and one within the routine being monitored (35). The fact that probes are implemented as code fragments that reside in the monitoring facility instead of the monitored application provides at least three advantages over sensors:

- They might have a more general view of the system.
- The monitored application code is unchanged so that information to be probed can be dynamically defined at run-time.
- Some researches show that program perturbation, compared to sensors, is reduced (34).

**2.1.4 Actuators**

When a certain condition is met certain parts of the system might need to be changed. For a change to be effective there must be a component who knows what to modify and how to do that. Such elements are called *actuators*.

As described in (18) there are a few aspects that can be involved in the change performed by the actuator. The *where* and *what* aspects of the change can be summarized by the following characteristics:

- **Layer** an actuator might affect one or more layers of the system.
- **Functionality** an actuator might affect one or more specific functionality of the system.
**granularity** the degree of change might be fine or coarse. It might involve whole components or smaller elements part of a component.

**cost and impact** Cost describes the impact on execution time, required resources, and complexity of change while impact describes the scope of the repercussions. Depending on how much the cost and how big the impact we can identify two major kind of adaptivity:

- *Weak* adaptation, which performs low-cost or limited-impact actions. Examples of weak adaptation are: modification of parameters, data caching, compressions, load balancing, change of algorithm.

- *Strong* adaptation, which performs high-cost or extensive-impact actions. Examples of strong adaptation are: replacement of whole components, change of architecture, provision of additional resources, redeployment of part of the system.

Monitoring involves obtaining and analyzing performance information from one or more components of the system. Optimization involves using the information to affect either the application or the system. Some examples of actions and actuators include:

- JIT (Just In Time) compiler optimizations (36)
- hot swapping operating system components (37; 38)
- dynamic update of an operating system (1)
- tuning of linear algebra (39; 40)
- redistributing application workloads
• selecting different algorithms based on input data (41) or on more complex factors (such as availability of processors and memory bandwidth) (42; 43)

• loop perforation (44)

• various parameters optimization: available memory, page size (45), OS page replacement policy (46)

• changing cache associativity

Even if we have explicitly divided sensing and acting, both activity usually occur at the same time in order to constantly evaluate the system and the effects of a change.

2.1.5 Characteristics of adaptation

There are several ways to embrace adaptation. The following lists, first proposed by (18), outlines the major differences:

**static versus dynamic decision making** The decisions might be statically defined (e.g.: encoded in a decision tree) or dynamically established at runtime. The parameters that can be involved in a dynamic scenario are: policies (47), rules (48), and QoS definitions (49; 50).

**external versus internal adaptation** Adaptation, in some cases, can be handled entirely by the application itself. This approach is rarely flexible, extensible, evolvable, and scalable. Adaptation often needs global information and for this reason an external approach is
Self-adaptive software sensing effecting

(a) Internal approach (b) External approach

Figure 5. Comparing internal and external adaptation

often adopted. Moreover an external adaptation engine can be more easily reused. This concept is shown in Figure 5.

making versus achieving adaptation Self-adaptivity can be introduced in a system at development time, when the system is engineered, or after deployment, through adaptive learning (12). These situations are referred to with the terms “making” and “achieving”, introduced by (51). The two approaches are not mutually exclusive.

close versus open adaptation Close adaptation has a fixed number of actions to be performed on the system while open adaptation provides the possibility of introducing new behaviors and alternatives.
model-based versus free adaptation When the adaptation process is evaluating a change it might make hypothesis based on a model of the environment and of the system. The model can be extracted in several ways: queueing models (52), architectural models (53), domain-specific models (54), etc. . . . Sometimes no model is needed and the adaptation process simply knows the goals and the possible alternatives. An example of model-free reinforcement learning has been shown in (28).

specific versus generic adaptation The adaptation engine might either address only specific situations or applications or be more generic and not focus on a restricted part of the system.

reactive versus proactive adaptation The adaptation engine might either react after an event has happened or try to predict it and behave accordingly. In particular the proactive solution is particularly useful when Quality of Service (QoS) is involved (50). However many solutions still do not offer real-time monitoring with QoS guarantees (55; 56; 57).

continuous versus adaptive monitoring The monitoring facility might either continuously observe the system or sample a few sensors and enable full monitoring only when irregularities are detected

human-performed versus automated actuation Humans might be required to perform some modification on the system. Obviously when the system can actuate most of the changes by itself it becomes more independent and, if trustworthy, more valuable. Manual intervention is extremely common in the enterprise domain; many rich monitoring solutions are available (i.e.: HP’s OpenView (58) and IBM’s Tivoli (59)) but they still
require humans to actuate the changes.

### 2.2 Monitoring and acting in autonomic systems

Autonomic systems as described above are based on the idea of monitoring and analyzing their performance and behaviors. Despite the rapid evolution of programming models and computer systems, performance analysis is still based on these steps (32):

1. *Application instrumentation*, which enables, either manually or automatically, sensors and probes on the application of interest

2. *Performance data extraction*, which captures data coming from all sensors and probes

3. *Analysis* and visualization of extracted data

4. Reasoning about the extracted data and subsequent *application optimization*

In the last decades we have assisted to a shift from *a posteriori* to *online* optimization. At first, applying (or actuating) a change on a system in order to improve its performance required humans intervention and for this reason it used to be called *steering*. Steering has a long history, especially in the field of super-computers where a complex system had to be monitored and managed along with its long running, resource intensive applications (60; 61). With the intent of automating such changes, application steering has first evolved into *auto-tuning*: the controlled entity started to self optimize at runtime adjusting statically defined parameters. Auto-tuning has then evolved to support dynamically defined parameters, joining the large family of autonomic application adaptation.
The scientific context has been the most exciting for this advancement since it was the first to adopt parallel systems, which proved to be extremely hard to tune. In this scenario, a good choice of parameters could really boost performance. Moreover, the rising complexity, the always growing users community, the diversity of applications, the number of radically different platforms and architectures targeted for optimization, and the consequent issues in portability and maintainability push toward the automatization of optimizations (62). For all these reasons parallel systems have long been studied (63; 64) and many projects have been developed: APIs to support both steering and autonomic monitoring (31; 61), a dynamic global resource management system (65), an adaptive application control of distributed applications (32), an instrumentation language for specifying tunable parameters (62), etc. . . .

In this field there is now a trend towards applying all the experience gained in the scientific field to consumer-level products.

2.3 Instrumentation

One of the problem that is linked with the monitoring of a system is instrumentaion. Instrumentation is the act of inserting either in the monitored application or in the monitor the fragments of code that represent sensors and probes. The term “instrumentation”, even if referred to the placement of sensors and probes, will often implicitly implicate the presence of a supporting monitoring architecture.

There are basically two approaches to instrumentation:
• *supported* instrumentation, where the executable is built with the possibility of executing statically or dynamically defined instrumentation code. It will be discussed in 2.3.3

• *forced* instrumentation, where the executable is unaware of the instrumentation which is pushed either statically or at runtime. It will be discussed in 2.3.1

There are some projects that provide *profiling* without making use of instrumentation. Two well known examples are:

**Oprofile** (66) It collects system-wide profile information without requiring any instrumentation (or any modification at all) to the compiled executables. It is based on hardware counters and for this reason it suffers the following shortcomings (3; 67; 68):

1. restricted number of counters
2. sampling delay
3. lack of address profiling
4. even on modern system, impossibility of reacting to single events
5. low level view does not easily translate into high-level application performance indicators

**dproc** (69) It extends Linux’s `/proc` with an efficient, hierarchically-organizer, cluster-wide, application-specific view of monitoring information about both local and remote cluster nodes. Even if its implementation offers high-performance it is not capable of meeting expectations for usages like program steering and adaptation. *dproc* has also been ex-
tended to become a highly-configurable QoS-capable monitoring system (50).

2.3.1 **Supported instrumentation**

This kind of instrumentation is supported by the availability of an *Application Programming Interface* (API). Programmers place function calls (or code fragments) in the source code of their applications; such snippets will call certain API functions with one of the following purposes:

1. having a certain event logged
2. sending an “I am alive” signal more often called *heartbeat* or *reflex signal*
3. asserting an expectation, for instance a performance expectation

2.3.1.1 **Tracing infrastructures**

In the past years the most common research projects provided an architecture to log events. An example of this is *relayfs* (70), a kernel API to transfer data from kernel to user space. *Relayfs* offers a lock-free implementation that scales well on multi-processor systems.

One of the first most-known tracing facility is the *Linux Trace Toolkit* (LTT) (71) that helps recording and analyzing complete system behavior. Through LTT every system event is recorded, making it possible to analyze the functioning of the system itself. LTT provides static instrumentation that causes a non-zero (yet small) probe effect; it defines only a limited number of statically defined events and it cannot take arbitrary actions. Moreover the events
have fixed length and timestamp acquisition is inefficient (72).

Even if implemented efficiently (< 2.5% when observing core kernel events), LTT has been out-classed by the K42 tracing facility. K42 (73) is an operating system developed by IBM and the University of Toronto since 1998. K42 is open-source OS which targets 64-bit multiprocessor systems (especially PowerPC architectures). It uses an object-oriented design to achieve good performance, scalability, customizability, and maintainability. K42 implements each system resource (i.e., an open file or a running process) as an object. Each object reference is stored in a global table (called object translation table, OTT) and this facilitates hot-swapping (38), since every object can be changed on-the-fly. This enables dynamic reconfiguration and adaptability of the system and, when combined with a kernel module loader, supports dynamic update (1) and hot patching of the OS.

K42 features a tracing facility (72; 74) that, just like LTT, has statically defined events. Yet it presents some more advanced characteristics, such as: extreme scalability, variable size events, circular buffer to avoid overflows, per-CPU buffer to perform efficiently on multi-processor systems, lock-less implementation, monotonically increasing timestamp mechanism, events classes, etc. . . . The K42 monitoring infrastructure is not considered safe because the integrity of traced data is not guaranteed.

DTrace (75) is probably the most advanced tracing technique available at present. It has zero probe effect, it can dynamically instrument both user-level and kernel-level code in a unified and completely safe way. Moreover it is scalable and it supports highly customizable actions
(which are described through a C-like language) at any given point of instrumentation. DTrace has been included in three operating systems: Sun Solaris, FreeBSD, and Mac OS X.

2.3.1.2 Assertions

Assertions augment tracing infrastructures creating a framework that not only collects performance-related data but that also verifies whether performance expectations have been met. Assertion-based projects in fact usually provide an API to be used when programming the target application: through such API users can describe their predictions and their hopes about their application running behavior. For each assertion the runtime system gathers the necessary data and calculates whether the assumption is verified. In an autonomic scenario the system might change its configuration in order to fulfill a certain assertion or it might change its configuration if one or more expectations are not met.

An assertion-based framework is not a new concept: it has been employed in a variety of older (76; 77) and newer projects (such as a self-healing framework (78)).

One of the most prominent assertion-based project is the performance assertions (PA) (79) system. PA was born from the need (also described in 2.2) of innovating performance analysis techniques: manual reasoning about raw performance data has proved to be tedious, difficult, error-prone, and inadequate considering the scale and the complexity of modern systems. For this reason PA has proposed a new methodology: programmers plant their expectations directly in the source code and the runtime environment gathers the necessary information to verify the assertions. Expressions can contain an assortment of tokens that represent empir-
ically measured performance metrics, constants, variables, mathematical operations, a subset of intrinsic operations, and format specifiers. Responses to failed assertions can take a number of different forms and are customizable through the specification of a user-defined subroutine.

PA is dynamic, (quite) portable, and has a limited overhead on runtime execution.

2.3.2 **Heartbeats**

Many of the solutions presented before provide good flexibility in monitoring a system but they do so at the expenses of simplicity and, sometimes, portability. Starting from this assumption the idea of simplifying sensors, probes and assertions as much as possible was born; this concept evolved into the *Application Heartbeat* (or, more simply, *Heartbeat*) (67; 68), a portable, general method of monitoring an application’s actual progress towards its goals.

This framework implements a simple yet extremely powerful monitoring infrastructure (the API is made of a small set of functions that makes it straightforward to use). Any application making use of its API has then a standardized method to:

- assert its performance goals registering to the *Heartbeats* and specifying a certain number of parameters: minimum and maximum heart rate, size of the window of observation, size of the heartbeats history buffer, and others.

- update at runtime the progress through the call to a function that signifies an heartbeat.

The framework automatically updates all the necessary information about the global heart rate, the windowed heart rate and other internal information.
• monitor the progress of the execution. The calculated information is made available to either external observers or the application itself (as shown in Figure 6).

A typical example of use is a video encoder, which might want to be able to deliver 35 frames per second. It then communicates this information to an Heartbeat API function and it issues an heartbeat for every produced frame, informing the framework of its progress. An external observer can consequently improve (or reduce) performance through the modification of some parameters, such as the number of cores assigned to the video encoder application. As shown in Figure 6, if needed, the application can impersonate the external observer and monitor itself. Obviously a monitor that resides outside of the application might have access to global information and parameters that are unavailable inside of the application.

The Heartbeat API is thread-aware, meaning that applications can set their performance goals either per-application, per-thread, or both.

The idea of “heartbeat” has its roots in Grid Computing(29), where pulses where used to constantly monitor the most crucial parts of a system through the emission of a regular “I am alive” signal. The Application Heartbeat API builds on this very simple mechanism, leaving untouched its simplicity, lightness, and generality.

2.3.3 Forced instrumentation

Forced instrumentation is a general technique that brings instrumentation to programs without recompiling them and, so, without requiring access (or any modification at all) to the source code. It useful when the source code itself is not available or when there is no willingness or
resources to rework it.

For this reason forced instrumentation is the only available option in case we intend to retrofit old applications (80) with self-adaptive characteristics.

One example of forced instrumentation can be developed in the context of the Application Heartbeat API (already described in 2.3.2), where it might be desirable to instrument application that will not be reworked to support the framework. Instrumentation might be automatized with the use of Intel Pin (81). This tool can inject arbitrary code (written in C or C++) in arbitrary locations of an executables. Unlike other solutions, Pin does not statically rewrite the executable with the instrumentation code but it rather adds it dynamically while the software is running.
Pin’s approach has been ported in kernel-space by the KernInst framework (82; 83), which allows dynamic splicing of (almost) arbitrary code into an (almost) arbitrary location of an executing kernel (Solaris or Linux) without recompilation. This framework work thanks to the kerninstd daemon, which dynamically instruments the kernel, and to the /dev/kerninst kernel module. In order to write a custom application that interact with kerninstd, it is necessary to use the Kerninst C++ API. Kerninst has zero probe effect when disabled but it has higher overheads than the K42 tracing facility (72) and it is considered “unsafe” (75) because it might cause fatal errors (in fact it allows users to instrument routines that cannot be instrumented).

Dprobes is a project similar to KernInst and it brings dynamic instrumentation not only to the kernel but, virtually, to any executable. It has zero probe effect when not enabled and it provides a language and a small virtual machine to execute actions in response to certain conditions. Even if DProbes has shown to have some concerns about safety (i.e., invalid loads are handled through an exception mechanism), the way dynamic instrumentation is implemented proves to be lossy when a condition is hit simultaneously on different CPUs. Moreover misemploying DProbes can lead to a system crash. DProbes is mainly used as a (Linux) debugger through which software probes are dynamically inserted into executing programs. When a probe is fired, a user-written probe-handler is executed. DProbes is particularly useful in extreme conditions since it makes it possible to read all the hardware registers, read/write some of them, and read/write any area in the virtual address space that is currently in physical memory.

An analogous project is DynInst (84; 83), which aims at manipulating programs at runtime.
DynInst is a C++ API that is used to attach to a program and add new bits of code to it. The program being modified does not need to be re-started, re-linked or re-compiled. The API also permits changing subroutine calls or removing them from the application program. The purpose of the DynInst API is to provide a machine independent interface to permit the creation of tools and applications that use runtime code patching.

A relatively recent tool is the Performance Counter Subsystem (PCS or perf) that has been introduced in the Linux kernel version 2.6.31. perf aims at helping both kernel and user-space developers in improving performance of their code, providing a range of mechanisms to determine code portions that need to be changed and highlight bottlenecks in current implementations. The Performance Counter subsystem provides an abstraction of special performance counter hardware registers available on most modern CPUs. These registers count the number of certain types of hardware events, such as executed instructions, cache misses, mis-predicted branches, and so on. This happens with a minimal impact on kernel and applications. These registers can also trigger interrupts when a threshold number of events have passed and can thus be used to profile the code that runs on that CPU. In the current release the x86 and PPC architectures are fully supported while support for S390 and FRV is in the works.

Forced instrumentation can also be applied to web applications, just like the case of AjaxScope (85), which parses and instruments JavaScript on-the-fly before it is sent to users’ browsers. AjaxScope builds on the idea of instant redeployment which is intrinsic to web applications (since every the application is dispatched when the user opens it); in the extreme every sin-
gle user might have a different version of the application. This is particularly useful for A/B testing, where some users are presented with “version A” of the program and some others with “version B”, and for distributed testing, where the burden of logging information for debugging purposes is spread along all the users. This proves extremely useful to gain visibility into errors occurring in users’ browsers that would otherwise remain unknown to the developers. This move the debugging capability to the end-user desktop, making it possible to obtain all the information that are available only at the moment of failure (86). AjaxScope, being implemented as a proxy server, does not require changes on the server-side and does not require installation of plug-ins on the end-user desktop. AjaxScope features drill-down monitoring which is a technique that iteratively and dynamically analyzes the badly performing functions in order to isolate the causes behind a potential malfunctioning. Static analysis might be employed as well in similar situations: even if not as powerful as dynamic analysis, it is useful to reduce sensors overhead removing some instrumentation points (87). A statistical and dynamical approach might be adopted to diminish the burden of dynamic analysis.

Other projects (in particular, BrowserShield (88) and CoreScript (89)) rewrite JavaScript “on-the-fly” but with the different purpose of enforcing browser security and safety properties: they might represent an important step in the direction of the self-protecting property.

2.4 Adaptation engine

While almost all of the researches presented before address the problems of instrumenting an application with sensors and developing an architecture that supports runtime dynamic
changes, the engine that should make decisions based on sensors data and should command the actuators is not as defined.

Many different approaches have been followed and, in general, it seems hard to design an engine that stay flexible over time. For this reason, AI-related techniques are more and more often employed.

As described in 1.3 many different fields might contribute in the creation of an adaptation engine. These are some of possible techniques that might be adopted:

- the rule-based approach is very common (79; 31) and it allows for a good degree of flexibility if the operating environment is well known and can be described through rules. Rules guarantee a fairly deterministic behavior and allow actions to be taken in response to the occurrence of an event.

- decision trees are generally more static and more hardly extendable than rules but they are usually more efficiently evaluated at runtime.

- pattern-matching is an AI concept used (65) to make the program attune to a context that is ever-changing yet is exhibiting some kind of periodic behavior.

- reinforcement learning is a sub-area of machine learning that might be used to implement the application as an agent aiming at maximizing the long-term reward. Reinforcement learning has already been used in some notable projects (28; 27).

- Fuzzy logic is known for working particularly well in the context of conflicting goals and poorly understood optimization spaces (32).
• *Game theory* might also be used to implement a non-centralized system.
CHAPTER 3

PROPOSED METHODOLOGY

“No matter how big and touch a problem may be, get rid of confusion by taking one little step toward solution. Do something.”

George F. Nordenholt

“When I am working on a problem I never think about beauty. I only think about how to solve the problem. But when I have finished, if the solution is not beautiful, I know it is wrong.”

Buckminster Fuller

This Chapter provides a general description of autonomic systems and introduces the proposed methodology, which will present the design of a decision engine that deals with sensors and several modular actuators.

3.1 Our vision

3.1.1 Overview

Related works show many examples of autonomic implementations: most of them are stand-alone projects, in that they are not part of a complete autonomic system.

MIT’s CSAIL, along with the Politecnico di Milano, is working to create such complete system
in the next few years. The vision is to modify and extend a Linux distribution with the aim of obtaining an operating system aware of the applications’ performance goals and capable of performing changes to itself both in user and kernel space. In this scenario there are many components that are going to be affected:

**Kernel-level actuators** The kernel might be extended to support mechanisms such as hot-swap (37; 38), which allows modules to be swapped at runtime.

**User-space libraries** New libraries might be added in order to support performance monitoring. The *Application Heartbeats* (67; 68) are a notable example of implemented frameworks, which can be used as the backbone of the monitoring infrastructure.

**System libraries** In order to improve performance some user-level libraries might be ported to the kernel. Obviously this would make them less portable.

**User-level services** There exist several software components that have the capability of improving and reducing performance (i.e., CPU frequency scaling). Such actuators, which are extremely different and numerous, suggest a standardization of their control interface.

**Application** They might have to be modified to make use of the new system libraries. There exist approaches (described in Chapter 2) that can be used to avoid to update any single application in the system: instead, monitoring sensors might be injected either statically or at runtime.
3.1.2 A system based on observing, deciding, and acting

In the theoretical view, an autonomic system can be called such when it implements the Observe-Decide-Act (also known as ODA and described in 2.1.1) model. In such model three roles have to be defined and will be described in the following three Sections.

The general realization of this system is shown in Figure 7.

3.1.2.1 Observe

Observation is what makes a system aware of itself. Through observation a system understands its state, its current progress, and its possible future actions. This introduces at least three levels of awareness:

1. awareness through the data gathered by sensors
2. awareness of the availability and potential of actuators
3. awareness of the possible targets of actions

Since the aim of the autonomic system is the improvement of applications performance, there needs to be a monitoring infrastructure that gathers the necessary data and fulfills the first kind of awareness described above. To keep track of performance the Application Heartbeats (already described in Chapter 2) seem today as the best possible choice: it is a simple yet powerful framework that allows software components (such as applications) to assert performance goals and keep track of the progress.

Other options, such as Autopilot (32) (an infrastructure for dynamic tuning of heterogeneous
system), *Harmony* (65) (an infrastructure to efficiently execute parallel applications in large-scale, dynamic environments), and *Orio* (90) (extensible annotation-based tuning system which triggers low level performance optimizations depending on the annotations present in the source code) have been considered but either aim at enterprise environments or are not as portable and as flexible as desired.

Regarding the second type of awareness described above, there needs to be a way for the system to find the available actuators and understand their potential in terms of system-wide or application-specific effect. Currently a similar mechanism has not been implemented; yet it will be necessary to introduce for the autonomic system to be complete.

Similarly, the possible targets aimed at by the actuators must be defined: such information might change dynamically and a mechanisms for determining them at runtime must be introduced for the autonomic system to be complete.

### 3.1.2.2 Decide

The *decision engine* stands at the center of the *ODA* loop. It is the element that exploits the awareness given by the observation mechanisms to elaborate a plan for future behavior. The aim is to improve performance as much as possible making each software component achieve its performance goals.

In particular, the *decision engine*: determines the monitored applications; gathers the information coming from one or more of them; if necessary, updates the list of available actuators; analyzes the performance-related data in order to understand whether to enact a correction
policy; possibly decides which actuators to activate or modify; submits the final plan to the actuators.

The role of the decision engine has not been implemented at present; yet it will be necessary to introduce it for the autonomic system to be complete.

3.1.2.3 Act

Once a strategy has been decided, it must be enacted. There are many kinds of actuators that might be available and that might affect performance in different ways: some might affect performance, accuracy, or both; some might impact on the whole system while others might target one or more applications; some might require changes in the application while others operate without the target being aware.

For instance: Unix niceness enhances performance leaving accuracy intact, while techniques such as loop perforation enhance performance altering accuracy, frequency scaling affects performance of the whole system while loop perforation can target a single application, and so on.

In an autonomic system we can define two main categories of actuators:

**Blind Actuators** This kind of actuators enact its action without knowing the actual effect on the system

**Conscious Actuators** This kind of actuators have an feedback mechanism to be aware of the results of its action
Figure 7. Consensus object role: (1) dynamically update the list of available actuators and possible targets; (2) decide which actuator to enable on which target
3.2 Consensus Object: design and surrounding architecture

3.2.1 The need for a Consensus Object

Section 3.1.2 describes the general autonomic architecture and highlights what still has to be implemented.

Those are the components that have to be created to have a complete autonomic architecture:

- A mechanism to discover and dynamically update availability and characteristics of actuators.
- A mechanism to discover and dynamically update the possible targets aimed at by the actuators.
- A central element that: (1) analyzes data coming from the applications, (2) decides which applications need to be targeted, (3) chooses which actuators to activate or to modify, and (4) submits the plans to the actuators.
- A standardized interface to easily interact with different actuators.

There is a strong need for a complete autonomic environment and this thesis aims at making the first step in this direction. In particular we believe that a central element is needed not just to “complete” the system but also to orchestrate the various services that would otherwise run independently and, possibly, with very few coordination.

The aim of this Chapter is to design the architecture of a system made of a decision engine, a set of services, the interface to the services, and a mechanism to discover new actuators.
3.2.2 Terminology

This Section presents some crucial definitions. Such terminology is extremely important and will be used throughout this dissertation.

**System** All the software and hardware components currently in use.

**Thread** It is an executable element contained inside a process. Multiple threads can exist within the same process; in such case they share resources (i.e., memory), while different processes are independent. The thread is the atomic entity in an operating system that can be given CPU time.

**Application** An application is a piece of software written to accomplish a specific task.

**Process** A process is an instance of an application; it might be composed of several threads that execute in parallel.

**Monitored Process** A process that is making one or more entities of the system aware of its performance goals and actual progress. This is possible, for instance, adopting the *Application Heartbeats*. Monitored Process are what has so far been called *targets* in that their performance is altered by *actuators*.

**Service** It is an extension of what has so far been called *actuator*. Not only does it represent a component capable of performing changes on one or more applications or on the system (affecting their performance) but it also includes the interface to interact with the decision engine and with the application.

**Consensus object** It is what has so far been called *decision engine*. It has a number of roles:
• It reads the heartbeats coming from one or more applications

• It has a mechanisms to retrieve a list of currently available services; it also is able of updating such list at runtime

• It features a mechanism to choose which services should be used on which terms

**Adaptive object** Any component in the system that operates through an **Observe- Decide-Act** loop.

In the context where applications set their performance goals, the **Consensus Object** orches-
trates the available service the boost (or reduce) performance in order to make it possible to achieve such goals.

### 3.2.3 Execution scenario

It is possible to envision two different scenarios, where one is the natural evolution of the other:

A. One application \((a)\) and multiple service \((s_i \text{ with } 1 \leq i \leq n)\)

B. Multiple applications \((a_j \text{ with } 1 \leq j \leq m)\) and multiple services \((s_i \text{ with } 1 \leq i \leq n)\)

Other scenarios such as **one application and one service** or **multiple applications and one service** are special cases of \(A\) and \(B\). Within this thesis we are considering only the first scenario. Nevertheless the architecture will be designed to support multiple applications. What will not be included is a mechanism to handle multiple applications goals in case the resources are not sufficient to make each application reach its goals. For this reason the proposed methodology
is multiple-application-compatible but cannot guarantee the correct balance of resources among applications.

3.2.4 A non-centralized approach

The Consensus Object represents a centralized approach: it is the central element that gathers all the performance information coming from the applications and coordinates services. Although it represents a single point of failure it is the only approach that allows to have a global overview of the system performance. Moreover, through a “smart” decision engine algorithms, it can really boost performance learning from experience the effect of each service. As a last consideration we would like to say that the Consensus Object will be designed in a way that, in case of crash, the whole system can keep on running almost normally. For this reason the services will be very independent in many aspects and this makes it possible for them to run also after the Consensus Object has abnormally quit. This characteristic is known as mitigation of the single point of failure.

An alternative to the centralized approach is a game-theory-like scenario where each service acts independently but can collaborate and communicate with other services in order to try to reach an optimal solution. This scenarios is certainly much more complex and currently seems overly elaborate to be implemented effectively. For this reason the centralized approach has been embraced and it has been chosen to develop a first implementation of a complete autonomic system. The game-theory-like scenario has not been discarded but it might be considered again once the implementation of the centralized version is complete: this is the only way to
understand which of the two actually works better.

3.2.5 A consideration about performance goals retrieval

In such scenario one uncertainty might arise: who establishes performance goals? And who is in charge of updating the progress towards such goals? The proposed solution makes use of the Application Heartbeats: this means that each application that adopts this framework autonomously sets the goals and updates the progress. This solution is perfectly reasonable since only the application knows what its goals are and when it has finished the computation of a fraction of information.

One might lift the potential problem of applications that deceive the system with intentionally wrong information. This might happen if the application wants as much resources as possible. Even though this is not an impossible situation, in our opinion it is not an issue because of the following:

- issuing less heartbeats, or providing an impossibly high performance goal defeats the purpose of self-adaptive computing
- the role of the consensus object is to mediate resources among processes; for this reason enough resources are guaranteed even when one of the processes is particularly demanding
- even in current non-autonomic systems a similar argument might be raised; in fact, a process can create hundreds or thousands of threads, can fork hundreds or thousands of times, or it can “nice” itself to obtain as much resources as possible
In response to these potential issues, we have developed *Pacemaker*, which is a tool based on Intel’s *Pin*. It analyzes the execution patterns of a given process, finding the loops where most of the execution time is spent; it then injects the code to produce heartbeats, making the given application (even if closed-source) heartbeats-capable.

### 3.2.6 Services

There is a certain amount of elements that can affect performance on a system. They come in different forms and they bring different consequences. This section presents a list of possible services and proposes a first classification.

**Application “knobs”** They are parameters in an application that can be changed, causing an alteration in performance. An example is the *number of threads*: there is not a best value overall but, depending on the load of the system and on the availability of computational cores, a wisely chosen value can improve performance. This usually is an application-specific optimization.

**Application implementations** Many applications make use of certain algorithms to carry out their duties; depending on the scenario a different implementation might lead to better results. For this reason a way to enhance performance could consist of changing the implementations of a certain algorithm. A new implementation of the swapping algorithms certainly needs to take into account the experience with K42 (38; 73; 91; 92). This usually is an application-specific optimization.
Core allocation Under modern operating system it is possible to link a given process to a certain processor or core. Ideally when each process is linked to a certain core and the number of cores is sufficiently high there would be no need for time sharing among processes. This optimization is enabled on a per-application basis.

Memory allocation The core allocation service is particularly useful when used on processes that are CPU-intensive. There exist another category of processes that requires, instead of processing resource, memory. Such programs are said memory-intensive: in order to be optimized they require memory to be allocated easily and efficiently. This optimization is enabled on a per-application basis.

Niceness adjusting Modifying the niceness (which is the priority of a process) can enhance or reduce performance of a given application. This optimization is enabled on a per-application basis.

Locks Changing the policies on the management of locks might also affect performance (93).

Frequency scaling It is a widely popular service that reduces processor clock frequency slowing down the computation on the entire system.

Dynamic perforation Some optimizations increase performance to the detriment of accuracy of calculation. For instance, this happens with dynamic loop perforation (44). This optimization is enabled on a per-application basis.

Those service can be classified in the simplest possible way: using the notion of application-awareness. Two categories emerge:
• services that can be enabled without the application being aware of them. Most of the services listed above are categorized this way. Services belonging to this category are: *application implementations, core allocation, niceness adjusting, locks, frequency scaling, dynamic perforation.*

• services that require the program to be aware of them. The only service belonging (so far) to this category is: *application “knobs”.*

One might not understand because *application implementations* are part of the first category. These two definitions will become straightforward once the communication infrastructure is analyzed in Section 3.2.7.

### 3.2.7 Communication infrastructure

In order to understand the communication infrastructure it is necessary to highlight the *information flows* (which will be called FN from now on) that have to take place in this autonomic system

F1. the *Consensus Object* has to know which applications make available their performance goals and their progress towards them

F2. the *Consensus Object* has to know about the heart rate of the application

F3. the *Consensus Object* has to know which services are available

F4. the *Consensus Object* has to control the services
F5. the application has to advertise the parameters (*application knobs*) that it intends to expose.

F6. each service has its own way to actuate a change. Some involve the communication of some kind of information back to the application; in the *application knobs* example the values of one or more parameters has to be communicated.

F7. possible implementations of a functionality must be known to some component in the system.

The described information has to be delivered for the autonomic system to be effective. Flows F1 and F2 will be described in Section 3.2.7.1. Flows F3 and F4 are delivered through apposite interfaces which will be discussed in Section 3.2.7.2. Flows F5, F6, and F7 can be delivered introducing two libraries (*Implementations library* and *Parameters API*) that will be discussed in Section 3.2.8.

Information flows F1, F2, F3, F4, and F6 are shown in Figure 8 (F5 and F7 are not shown because they require concepts that will be introduced only later).

### 3.2.7.1 Monitored applications information flows

For an application to be able to communicate its goals two steps have to be performed:

1. the *Consensus Object* has to become aware of the fact that one or more processes are making use of the *Application Heartbeats* to set their goal and update their progress; this is possible thanks to F1
Figure 8. General concept behind informations flows $F_1$, $F_2$, $F_3$, $F_4$, and $F_6$ in a multi-service and multi-application scenario
2. the *Consensus Object* has to be able to access the information recorded through the

*Application Heartbeats*; this is possible thanks to *F2*

*F1* is also known as *application registration* and it might be implemented exploiting the fact that, for each application that makes use of the framework, the *Applications Heartbeats* create a temporary file that is deleted when the process either terminates or unregisters from the *Applications Heartbeats*.

*F2* is straightforward since the *Applications Heartbeats* share a memory segment where data is stored and makes it possible to access such information with an apposite set of functions: through them it is possible to set an “heart rate monitor” that enables the *Consensus Object* to read the collected data and statistics.

### 3.2.7.2 Service registration and control interface

Two steps are needed for a service to be active:

1. the service must advertise its existence to the *Consensus Object*

2. the service must implement a standard interface in order to receive commands from the

*Consensus Object*

In the two following Sections these two steps will be thoroughly explained.
3.2.7.2.1 Service registration

The Consensus Object has to be aware of the services that are available in the system and it also has to know a few information to characterize them. It should in fact know the main traits of the service, such as: name, granularity (whether it affects the whole system or can target single applications), and so on. In order to exchange this preliminary information a common location must be defined. Given that:

- the Consensus Object and the services run as separate process
- their startup order is not known (we do not assume the Consensus Object to be already running when one or more services start)
- the PID of the Consensus Object and the services are not known a priori

it is necessary to hard-code in both the Consensus Object and the service a common location. This can be accomplished exploiting files or named pipes: for instance, in a folder one file per each available service might be written (similarly to what happens in the Applications Heartbeats for the monitored applications); or a named pipe might be used to write and read available services.

Through this preliminary communication channel the service might inform the Consensus Object of its name, PID, and all the information needed to establish a new channel where to exchange control directions. The chosen solution should be flexible in that it should work independently from the startup order of the Consensus Object and the services: files and pipes seem to be a good answer since the service advertises its existence right after startup. The
Consensus Object periodically reads from it and updates the list of available services. If the Consensus Object has not started already it will read the list right after its startup.

The registration mechanism is identical among each service: such characteristic is necessary to keep the implementation as modular, expandable, and flexible as possible.

\subsection{3.2.7.2.2 Service interface}

Registration is necessary to establish the communication channel through which the Consensus Object controls the service. Such channel might be arranged as a \textit{shared memory segment}. In this scenario the initial communication should be used to exchange the key to be used to access the shared memory segment, which has already been allocated by the service. When the Consensus Object needs to control the service it simply writes to such segment, specifying a high-level command and a target. Examples of commands might be \texttt{increase\_performance} and \texttt{decrease\_performance} or \texttt{enable\_service} and \texttt{disable\_service}. An example of target might be the PID of a process, which has to be affected by the service.

The PID has to be communicated because the service, in most cases, will be very general and might target different processes in different ways. For instance, dynamic perforation might be used on a process and not used on others. Obviously, this kind of information is not needed in case the service has an impact on the whole system.

Each service, regardless of the kind of optimization it performs, is treated in the same way by the Consensus Object, which commands the service through high-level commands. This way the definition of service remains particularly general and allows a great degree of modularity:
future services can be implemented without necessarily updating the Consensus Object.

Figure 9 shows what was explained in this Section.

3.2.8 Autonomic libraries

Information flows $F5$, $F6$, and $F7$ can be explained after a short premise. One of the purposes of autonomic computing is to reduce the burden on programmers, hiding some of the complexity they would have to deal with if they had to implement a self-adaptive system from scratch. Some possible uses of autonomic computing seem particularly promising: for instance,
the capability of self-tuning some crucial parameter of the system \((i.e., \), the number of threads in an application\); or the capability of choosing at runtime the best implementation of a given functionality.

These two examples should be made available to the programmer with the least effort on his or her side. Our vision is to provide such functionalities introducing two libraries: the Implementations library and the Parameters API. Following this approach, newly built applications can make use of them and easily become part of the autonomic system.

Libraries are services themselves. In order to make the optimal choice they have to communicate with a central entity, the Consensus Object, that has a global view on the system. For this reason both the libraries register to the Consensus Object and implement the service interface. The Consensus Object commands will then be translated in meaningful parameters or in a possible implementation switch.

Implementations Library is a general term to refer to a library that exposes some functionalities and has the capability of switching among implementations. Suppose an application needs to sort a very big set: instead of writing the code to perform such action the programmer might rely on an external library (that obviously exposes the required methods). Since there are many algorithms that order a set and they all perform differently, it would be desirable to have the library automatically pick the best one. Obviously abstracting such complexity from the programmer is highly advantageous. Yet, for a correct choice of the implementation the library must have data regarding performance. For this to be possible it has to open a channel with the Consensus Object, register as a “normal” service, receive the commands, and obtain the
Figure 10. (1) Implementations library; (2) Parameters library
necessary information about performance exploiting the heart rate monitor. The Consensus Object will then control the library just like it would do with any other service, through high-level commands. The library knows the available implementations (F7 is then totally internal to the library) and, given such information, picks the best one.

The mechanism that handles the change of implementation is called hot-swap. Hot-swap can be defined as the dynamic insertion and removal of code in running systems. It usually consists of two pieces:

- **Interpositioning**, which involves inserting additional components between existing ones; such components usually assist the transition.

- **Replacement**, which allows an active component to be switched with another component while the system is running, and while applications are using the resource managed by the component. This allows components suited to a particular environment to be switched as conditions change and allows new upgraded components to replace existing ones.

**Hot-swap** is a technique that has been widely discussed in recent research studies, especially those concerning the K42 Operating System (38; 73; 91; 92; 72). Its aims can be numerous: security patches, bug fixes, introduction of additional system monitoring, testing, and so on. In our work we will make use of hot-swap only to deal with implementation changes.

The whole K42 makes an extensive use of object-oriented technologies and each resource is managed as an object instance. Each object in K42 is mapped in a particular table known as Object Translation Table (OTT). Such table is the basis for hot-swap since simply replacing the reference to an object makes it possible to change the implementation of the given object.
Yet there are many aspects that have to be taken into account to ensure that the transition is smooth and guarantees data integrity. For this to be possible K42 researches have identified three essential issues:

**Quiescent state** Before it is possible to *hot-swap* the involved components must be brought into a safe state. The swap can only be done when the component is not currently being used. One way to achieve a quiescent state is to require all clients of the component to acquire a reader-writer lock in read mode before any call to the component. However, this would certainly add overhead for the common case and the lock could not be part of the component itself.

**State transfer** When it is safe to perform the *hot-swap*, the system has to decide what state needs to be transferred and how to transfer it to the new component. Such transferral should not only be safe but also as efficient as possible.

**References swap** In case the swapped element is referred by its clients (for instance, to allow bidirectional communication), the system has to know how to swap all of the references held by the clients of the component so that the references refer to the new component. In a system built around a single, fully typed language (for instance, Java), this could be done using the same infrastructure used for the implementation of the garbage collector. However, this might prove to be prohibitively expensive when dealing with few components switches. An alternative would be to partition a hot-swappable component into a *front-end* component and a *back-end* component, where the front-end component is referenced (and invoked) by all the clients, and its only function is to forward re-
quests to the back-end component. There would then be only a single reference (inside the front-end component) to the back-end component that would require to be changed when a component is swapped. This kind of implementation seems reasonable but it of course adds extra overhead to the common case.

K42’s proposed methodology is then based on the following steps:

1. instantiate the replacement component
2. establish a quiescent state for the component to be replaced
3. transfer state from the old component to the new component
4. swap the new component replacing all references
5. deallocate the old component

A simple representation of this procedure is shown in Figure 11.

An example of Implementations library has been described, implemented, and tested as part of this work: as it will be described in Section 4.2, the context of use provides the opportunity of simplifying the swap mechanism making use of a shared data structure and thus moving away from the more general case.

Similarly the Parameters Library is an API to expose certain parameters to have them controlled by an external entity. Since it is not advisable that the Consensus Object knows every possible parameters (and the effect that each of them can have on the system), the library will simply register to the Consensus Object as a service and implement the service interface to be controlled by the Consensus Object. It will then translate high-level Consensus Object commands into
Figure 11. (1) Normal operation with object L1 (that is referenced in the Object Translation Table, OTT). (2) L1 has to be replaced by an improved or optimized version called L2. First of all L2 is created, L1 reaches a safe state, and then L1’s state is transferred to L2. (3) References are updated and L1 is deleted.
Figure 12. Global view of the information flows. The picture shows potential ways to deliver the flows defined in Section 3.2.7

changes in the parameters. $F_5$ is then a flow going from the application to the library and $F_6$ is going in the opposite direction.

Figure 9 shows the interaction between the Consensus Object and the two proposed libraries.

Figure 12 summarizes what has been discussed so far: for each information flow identified above a possible implementation is given.
3.2.8.1 Decision algorithm

The Consensus Object has to decide which services to activate on a given target. It should also be able to decide when to disable a service. The Consensus Object does not know which is the potential of each service (i.e., how much a given service can improve performance) and, more in general, it does not have a thorough description of the service characteristics. The Consensus Object at most knows whether the service can improve performance (in such case it is an UP service), decrease performance (in such case it is an DOWN service), or both (in such case it is an UPDOWN service). This means that, at least at the beginning of the execution, all services are equal to the Consensus Object. The ideal algorithm would have to evince from past behavior what services are most appropriate in a certain situation. This would of course make it easier and transparent to the services to identify services clashes, caused, for instance, by a contention on resources.

Such “smart” algorithm would have deep roots in Machine Learning techniques and would certainly require quite a big effort to develop. Since the focus of this dissertation is on developing the infrastructure for a complete autonomic system, an engine that is complex, flexible, and capable of learning from experience is left as future work.

At present a simple algorithm has been designed and implemented: we believe that it works well in the base case and help us to deliver not a finished product but an enabling technology that can be used as the foundation for future projects. The functioning of the algorithm is based on a static decision tree (shown below):
• The *Consensus Object* goes through all the available processes; please note that, as explained in previous Sections, this implementation of the *Consensus Object* is multi-application aware but it does not make any particular consideration when more than one application is monitored. This means that in the multi-application scenario an optimal solution is not guaranteed.

• When an application is under-performing (or over-performing), all the available services are scanned to see whether there exists at least one that can be enabled (or disabled) to make performance return in the application’s range of heart rate. If it exists, the *Consensus Object* sends an appropriate command to it.

There exist several types of commands that the *Consensus Object* can issue:

**ON** Turns the service on. The service usually starts its action and also starts monitoring the heart rate in order to *try* to make the conciliate its action with the obtained results

**OFF** Turns the service off.

**DOWN_BY (and UP_BY)** They are commands that might be useful in the multi-application scenario. The purpose of *DOWN_BY*, for instance, is to ask the service to reduce its action. This might be useful to either make the service release resources or make it intentionally underperform to reach a global optimum. In fact, when multiple applications are running, some applications might not be able to reach their goals: in this case one application might have to be penalized in order to make the others closer to their minimum heart
rate. The way this commands work is by forcing the service to believe that the heart rate goals has been reduced by a certain percentage.

Is the current heart rate between min and max?

Yes

No

Is it below min?

Yes

No

Wait and retry  Pick a UP/UPDOWN service  Pick a DOWN/UPDOWN service

3.2.9 Decision policies

An important topic that has not been raised in previous Sections is the need for good decision policies that should drive both the Consensus Object and the services. In fact there are many decisions that can be taken either with an heuristic or with a more elaborated calculation. The core allocator example is certainly emblematic. When performance drops under a certain threshold (i.e., the minimum heart rate specified by the application) it is needed to decide how to react to improve performance. To increase performance more cores could be allocated to the application; unfortunately the new number of cores assigned to the application is not straightforward. It might be enough to allocate one core in addition to the ones already allocated; yet maybe the decision policy might exploit what is known from the control theory and use a PID controller.
It seems reasonable to gather all these decision policies in an additional API that the Consensus Object and services can access. Figure 13 illustrates the idea.
CHAPTER 4

PROPOSED IMPLEMENTATION

“When the solution is simple,

God is answering.”

Albert Einstein

In this Chapter the proposed implementation will be thoroughly described. First of all, an example of Implementations Library will be outline. Then, more details on the Consensus Object and Services API will be provided.

4.1 Implementation considerations

Throughout this Chapter some principles will be constant in every implemented program. Whenever possible the realizations will be as modular, extensible, flexible, portable, and maintainable as possible.

4.2 DES: an example of Implementations Library

This Section presents an FPGA-based implementation of an autonomic system which features several self-* properties: self-awareness, self-reconfigurability, and self-optimization.
4.2.1 General description

This implementation of an FPGA-based self-aware adaptive computing system blends techniques developed in different research fields, such as monitoring, decision making, and self-adapting. The chosen approach merges the potential brought by reconfigurable hardware with the state-of-the-art in performance assertions, monitoring, and adaptation: FPGAs offer tremendous computational power while the possibility of running an operating system on top of it makes it possible to observe performance and take actions that can involve both software and hardware adaptation.

We have developed a mixed hardware/software system that monitors performance through opposite software components while providing both hardware and software implementations of the same functionality.

The system is in charge of choosing at runtime among the set of possible implementations (hardware or software among the available implementations) according to different criteria, such as expected performance, available area (set of resources) on the FPGA, input data type and size, functionalities already implemented and available as hardware components.

The purpose of the proposed architecture is to encrypt data using the DES algorithm as fast as possible. For this to be possible the system must be aware of the possible paths of executions and must choose not only with the static information that it might have but also with the total set of dynamic (calculated at runtime) data and statistics. A complete Observe-Decide-Act (ODA)-based (refer to Section 2.1.1 for more information) system was designed:

- observation is carried out through the Application Heartbeats
• decisions are taken through a simple adaptation engine, which is fed with the data gather through the Heartbeats
• actions are obtained through the hot-swap of the available implementations

In particular the system features two different implementations of the DES algorithm: a software and a hardware implementation.

The three ODA phases have been implemented within the proposed system, by using different software libraries. Figure 14 presents the overall structure of the proposed system.

4.2.2 Implementation Switch Service

In the presented implementation the component capable of analyzing the data gathered through the Heartbeats, taking decisions, and performing actions is called Implementation Switch Service (ISS). It queries the Application Heartbeats framework to have knowledge of the progress of the execution and obtain an overall history of the heart rate. Fed with such information, the decision mechanism chooses at runtime the best implementation to use in accordance to given constraints (such as the need to avoid oscillations) and goals (such as the desired heart rate).

The need for a dynamic choice between available implementations is given by the fact that the system is live and lives in an unpredictable environment: for such reason it is impossible to decide statically which implementation proves to be the most convenient. For instance, a static analysis might show that for any given input data size there exists an implementation which outperforms the others; yet many other factors (such as the variable system load) might
Adaptive Hardware Architecture

Operating system (Linux-based)  Hardware implementation(s)

Software implementation(s)  Implementation Switch Service  Heartbeats API

FPGA Architecture

Application

Figure 14. Self-Aware Adaptive computing system

prevent the static analysis to get real. For this reason the system must monitor the surrounding context and take decisions accordingly hence dynamically balancing all the constraints.

For self-aware adaptive computing system the ability to switch between different implementations of the same functionality while the system is running proves to be fundamental. The process through which this happens is called hot-swap. The hot-swapping of an implementation with another one is a non-trivial process; threads within a single process can access data structures concurrently and different implementations can use completely different data struc-
tures; state quiescence and state translation are the most visible problems the hot-swap process generates. As already described in Section 3.2.8, in self-adaptive computing literature this is a well-known problem and a general framework to solve it has already been proposed in (92). This general framework inspired our hot-swap mechanism which is divided in 3 sub-phases as illustrated in Figure 15. These three sub-phases are:

a) a prior phase representing a common working condition;

b) a transfer phase in which new requests are blocked in order to reach a quiescent state which is translated to fit another data structure;

c) a final phase in which both blocked and new requests are allowed to proceed.

Figure 15. Hot-Swap mechanism
Even though such approach is inspired by the state-of-the-art it differs from it since it works solely on data structures instead of objects. This is due to the fact that not all implementations of a certain functionality must conform to a single interface. Furthermore, this approach defines a technology able to manage also the adaptation of the underlying physical architecture using, where possible, hardware implementations.

4.2.3 System overview

It is indeed true that many problems are more efficiently solved using hardware implementations instead of software implementations. Yet this claim actually depends on the expected Quality-of-Service (QoS) hence a software implementation might perform sufficiently well with respect to given constraints. Therefore two DES implementations were designed, one in software and one in hardware. The applications specify an expected performance goal over time (e.g., 1000 blocks per second), the library translates the goal into an expected heart rate while the current heart rate is updated every time a block is computed. The Application Heartbeats makes it possible to check if the current heart rate fits the expected goal enabling the library to take decisions in accordance using an heuristic that avoids short-term oscillations. When the throughput (i.e., heart rate) over a certain window of time drops under or excessively overcomes the expectations (and when other more subtle conditions are true) the library acts to improve performance. Since it is aware of the presence of two implementations the action consists in an hot-swap between them.

To avoid incurring in the state translation problem described in the previous section the library
has been carefully designed in that the two DES implementations use the same underlying data structure. Moreover, since there is no need to change the underlying data structure the state quiescence problem is not raised and the library is not required to force the data structure into a stable state before the hot-swap is performed.

In more complicated applications the underlying data structure cannot be kept the same across different implementations. When this happens the state translation problem becomes real (e.g., the translation from a list to a tree and vice versa is not straightforward and it strongly depends on how algorithms manages these data structures) and might become even worse if data structures are accessed by more than one thread concurrently since the state quiescence problem raises too.

The advantage of this library is that it hides complexity from the applications which are unaware of the presence of multiple implementations and are only required to set an expected performance goal.

4.2.4 des-fpga code highlights

We have developed a stand-alone proof-of-concept application called des-fpga to evaluate our approach. The following Sections present the essential parts of the code.

4.2.4.1 Help screen

Even though des-fpga is just a proof-of-concept it provides several command line options through which to control many of the underlying implementation parameters.
Listing 4.1. des-fpga

You can launch with no parameters: the program will run indefinitely and encrypt/decrypt the same data over and over

Those are the possible parameters:

--iter or -i: specify the number of iterations of the program
--min or -m: minimum heartbeat rate
--max or -M: maximum heartbeat rate
--window or -w: window size
--buffer or -b: buffer depth
--swap or -s: minimum swap time among implementations
--hevery or -e: specifies the number of blocks after which an heartbeat is issued
--allow or -a: allow multiple switches
--nohwreconfig or -r: does not try to access hardware reconfiguration
--triple or -t: use Triple DES
--decrypt or -c: decrypt after encrypting
--log or -l: prints heartbeat rate on screen
--debug or -d: enables extensive logging
--nohb or -z: disables heartbeats
--onlyprinttime or -p: suppresses most prints (not execution time)
--time or -x: measure execution time
--version or -v: prints version and exits
--help or -h: prints this help and exits

4.2.4.2 Structure of the program

In this Section some excerpt of the code are shown. The provided comments (along with comprehensible variable names) should make it straightforward to understand.

Listing 4.2. des-fpga

```c
/* des-fpga.c */
[...]
#include "des.h"
```
```c
#include "heartbeat.h"

static unsigned char key_data[N][8] = {
    {0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00},
    [...] 
};

static unsigned char plain_data[N][8] = {
    {0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00},
    [...] 
};

static unsigned char cipher_data[N][8] = {
    {0x8C,0xA6,0x4D,0xE9,0xC1,0xB1,0x23,0xA7},
    [...] 
};

/* Global heartbeat struct */
heartbeat_t heart;

/* Hold command line parameters */
typedef struct {
    int iterations;
    double min_target;
    double max_target;
    int64_t window_size;
    int64_t buffer_depth;
    int64_t minimumSwapTime;
    unsigned int allowMultipleSwitches;
    unsigned int infiniteLoop;
    unsigned int log;
    unsigned int debug;
    unsigned int useTripleDES;
    unsigned int alsoDecrypt;
    unsigned int hardwareReconfigAvailable;
    unsigned int heartbeatsEnabled;
    unsigned int measureTime;
    unsigned int onlyPrintExecutionTime;
} parameters_struct;
```
parameters, struct parameters;

int main(argc, argv)
/* Main function */
/* After analyzing command line parameters it launches DES */
/* Depending on the command line parameters it will either */
/* indefinitely encrypt and decrypt the same data or do so */
/* for a limited number of iterations */
/* In the meantime it provides one heartbeat per encrypted */
/* or decrypted block. */
/* When the heartbeat rate is too low the implementation is */
/* swapped (this part still is not implemented) */
int argc;
char *argv[];
{
    [...] 

    /*** Command line parameters analysis ***/
    processCommandLineParameters(argc, argv);

    /*** Opens hardware DES device node ***/
    if(parameters.hardwareReconfigAvailable)
    {
        hw_des_fd = setupHardwareDES();
        if(hw_des_fd < 0)
        {
            parameters.hardwareReconfigAvailable = FALSE;
            printf("Tried to open FPGA dd... failed ...\n");
        }
    }

    if(parameters.heartbeatsEnabled)
    {
        /*** Heartbeat Setup ***/
        /* Heartbeat environment variable check */
        if(getenv("HEARTBEAT_ENABLED_DIR") == NULL ||
```c
strcmp(getenv("HEARTBEAT_ENABLED_DIR") , "")==0)
{
 [...]
}

/* Initialization of heartbeat API */
/* Arguments of heartbeat_init:
 * pointer to heartbeat_t
 * min heartbeat target
 * max heartbeat target
 * window size
 * buffer depth
 * log name */
int returnValue = heartbeat_init(heart, parameters.min_target,
parameters.max_target, parameters.window_size,
parameters.buffer_depth, NULL);

}  

/*** DES HW/SW Switcher ***/
/ Take start time */
if(parameters.measureTime)
    timeBegin = getCurrentTime();
lastSwapTime = getCurrentTime();

while((parameters.infiniteLoop || counterLoops < parameters.iterations))
{
    for (i=0; i<NUM_TESTS; i++)
    {
        /*** Software implementation ***/
        if(implementationInUse == SOFTWAREIMPLEMENTATION)
        {
            /* Encryption */
            if (((j=des_key_sched((C_Block *)key_data[i]),ks)) != 0)
            {
                printf("Key_error_k\%2d\%d\n", i+1,j);
                err=1;
            }
        }
```
memcpy(in,plain_data[i],8);
memset(out,0,8);
memset(outin,0,8);

if(parameters.useTripleDES == FALSE) /* Normal DES (1-DES) */
{
    des_ecb_encrypt((C_Block *)in,(C_Block *)out,ks,DES_ENCRYPT);
    if (memcmp(out,cipher_data[i],8) != 0)
    {
        [...]
        err=1;
}

if(parameters.alsoDecrypt==TRUE)
{
    des_ecb_encrypt((C_Block *)out,(C_Block *)outin,ks,DES_DECRYPT);
    if (memcmp(in,outin,8) != 0)
    {
        [...]
        err=1;
    }
}

else if(parameters.useTripleDES == TRUE) /* Triple DES */
{
    des_ecb3_encrypt((C_Block *)in,(C_Block *)out,ks,ks,ks,DES_ENCRYPT);
    if (memcmp(out,cipher_data[i],8) != 0)
    {
        printf("Encryption error,%2d\n"%k=%s,p=%s,o=%s,act=%s\n", 
i+1,pt(key_data[i]),pt(in),pt(cipher_data[i]),
pt(out));
        err=1;
    }

    if(parameters.alsoDecrypt==TRUE)
    {

```c
156     des_ecb3_encrypt((C_Block *)out,(C_Block *)outin,ks,ks,ks,DES_DECRYPT);
157
158     if (memcmp(in,outin,8) != 0)
159     {
160         printf("Decryption error \%d\n\%s\%s\%s\%s\%s\%s\n", i+1,pt(key_data[i]),pt(out),pt(in),pt(outin));
161         err=1;
162     }
163     }
164 }
165 }
166
167 }  /******** Hardware implementation ********/
168 else if(implementationInUse == HARDWAREIMPLEMENTATION)
169 {
170     if(!parameters.hardwareReconfigAvailable)
171     {
172         err = -1;
173         printf("Hardware reconfiguration is not available ... sleeping\n");
174         sleep(3);
175     }
176     else
177     {
178         if(parameters.debug==TRUE)
179             printf(" Starting HW configuration ...\n");
180             hardwareDES(i, hw_des_fd);
181     }
182 }
183 }
184
185 if(err!=1 && parameters.heartbeatsEnabled) /* no errors */
186 {
187     heartbeat(&heart, heartbeatNum++);
188     heartbeatNum = [...
189 }
190 }  // end of for
191
192 if(parameters.heartbeatsEnabled)
```


```cpp
{  
    /* Check heartbeat rate */
    /* Only when at least MINIMUM_SWAP_TIME instants are elapsed */
    currentTime = getCurrentTime();
    if( (int64_t)(currentTime−lastSwapTime) > 
        (int64_t)parameters.minimumSwapTime) 
    {
        globalRate = hb.get_global_rate(&heart);
        windowedRate = hb.get_windowed_rate(&heart);

        if(implementationInUse == SOFTWARE_IMPLEMENTATION 
            && (windowedRate < heart.state−>min_heartrate || 
            windowedRate > heart.state−>max_heartrate) 
            && (parameters.allowMultipleSwitches==TRUE 
            || swappedAtLeastOnce==FALSE) 
            && parameters.hardwareReconfigAvailable==TRUE ) 
        {
            /* Switching to hardware implementation... */
            implementationInUse = HARDWARE_IMPLEMENTATION;
            lastSwapTime = currentTime;
            swappedAtLeastOnce = TRUE;
        }
        else if (implementationInUse == HARDWARE_IMPLEMENTATION 
            && (windowedRate < heart.state−>min_heartrate || 
            windowedRate > heart.state−>max_heartrate) 
            && (parameters.allowMultipleSwitches==TRUE 
            || swappedAtLeastOnce==FALSE) ) 
        {
            /* Switching to software implementation... */
            implementationInUse = SOFTWARE_IMPLEMENTATION;
            lastSwapTime = currentTime;
            swappedAtLeastOnce = TRUE;
        }
    }
}

counterLoops++;
if(counterLoops > parameters.iterations) counterLoops = 0;
```
4.3 Services API

4.3.1 Functions

The Services API is made of several functions to initialize, register, receive commands, unregister, and shut down the service that makes use of them. In order to boost modularity of the system the functions are the same one for each possible service. Those are the cardinal ones:

init_service() This function takes care of the many details that have to be setup for the service before it can operate. It initializes all the data structures (in particular, service_state
which hold the state of the service) and allocates the memory where the Consensus Object will write its commands.

`register_service()` It opens the default named pipe (currently `/tmp/consensus`) and write its registration details: name of the service, PID of the service, memory key to access the interface where the Consensus Object will write its commands, type of the service (UP, DOWN, or UPDOWN). This function is blocking in that until the Consensus Object does not start up and read from the pipe the `write` does not return.

`rename_service()` It allows to change the default name, which is “service-PID”

`read_command()` This function allows the service to check whether the Consensus Object has issued a new command. This function returns `true` or `false` depending on whether there is a new command or not. The commands coming from the Consensus Object are high-level commands since they are common to all services. A command can enable a service, disable it, or ask it to release resources. In the latter case a special technique is used to reduce the heart rate that the service sees.

`remove_all_dead_processes()` This function is used to constantly update the list of processes on which the service has effect. This is needed to avoid accessing a shared memory segment (the one where all the heart rate information is stored) that does not exist anymore (and that would probably lead to a crash) because the process has quit. This is very likely to happen since the service might still be accessing this portion before the Consensus Object manages to turns the service off on that particular target.
unregister_service() and shut_down_service() Those two functions are used to when the service is going to quit. Through the former function the Consensus Object removes the service from its internal list; through the latter function shared memory is cleaned up (which proves to be essential on systems such as Mac OS X where the default maximum shared memory size is particularly small).

4.3.2 Service guidelines

To adopt the Services API, a service has to correctly use a certain number of functions. This Section aims at guiding developers in build a service which is complaint to the proposed architecture:

- it is suggested that the service handles ctrl+c and, more in general, the SIGTERM and SIGINT signals. The signal handler should be used to set a flag (possibly of type sig_atomic_t, so that an atomic assignment is guaranteed); the main loop of the program should check it and, when set, exit the loop. This kind of procedure is necessary to correctly unregister the service (even if the Consensus Object constantly checks all registered services and processes to ensure that they have not quit without warning) and deallocate memory correctly

- the service should declare a variable of type service_state_t and initialize it

- the service should register with the apposite function
• the service should enter the main loop of execution. At each iteration the following actions have to be performed:

  – check whether there is a new command available
  – update the list of processes taking into account that some of them might have quit
  – obtain the lock to access information about the processes
  – for each targeted process obtain the heart rate information and enact the action policy typical of the service
  – release the lock
  – check whether the signal handler has been invoked

• when the service exits the loop (i.e., because the user has pressed \texttt{ctrl+c}) unregister the service and shut it down

The code for an “template” service is shown below. In the future we hope to make the functioning of the \textit{Services API} more automatic.

```
/*
* Service example
*/

#include "services.h"
...
#define SERVICE_ACTION_INTERVAL 1000000
#define VERSION "1.0"
```
/ * Global variable to stop execution */
/* It is volatile because gcc was performing weird optimizations (thanks Filippiello) */
volatile sig_atomic_t should_quit = 0;

/* Handles SIGINT adn SIGTERM (catches ctrl+c) */
void kill_handler(int signal_number) {
    should_quit = 1;
}

/* Main */
int main(int argc, char** argv) {
    printf("Starting example service _ _version_ _%s ...\n", VERSION);
    boolean return_value = 0;

    /* Local variables */
    service_state_t *service_state = (service_state_t *)malloc(sizeof(service_state_t));
    service_command_t *latest_command = NULL;
    process_service_record_t *current_process = NULL;
    double windowed_rate;
    double min_rate;
    double max_rate;
    double desired_rate;
    double global_rate;

    /* Catches ctrl+c and shuts down cleanly : */
    struct sigaction sa;
    memset(&sa, 0, sizeof(sa));
    sa.sa_handler = &kill_handler;
    sigaction(SIGTERM, &sa, NULL);
    sigaction(SIGINT, &sa, NULL);

    /* Initialization */
    return_value = init_service(service_state);
if (return_value == false) {
    printf("Could not initialize service ...\n");
    exit(-1);
} else {
    printf("Service successfully initialized ...\n");
}

/* Service-specific initialization */
...

/* Change service name */
rename_service(service_state, "service-example");

/* Register to the consensus object */
return_value = register_service(service_state);
if (return_value == false) {
    printf("Could not register service ...\n");
    exit(-1);
} else {
    printf("Service successfully registered ...\n");
}

/* Main loop */
printf("Entering main loop of service ...\n");
while(1)
{
    /* */
    if (true==read_command(service_state, &latest_command))
    {
        /* Analyze command and possibly change behavior */
        /* Service specific code here */
        ...
    }

    /* Obtain lock because service is going to modify the shared segment */
pthread_mutex_lock(&(service_state->interface->service_interface_mutex));
/* Updates list of processes */
remove_all_dead_processes(service_state);

/* Act - for each process still alive */
int n=0;
for (n=0; n < service_state->interface->index ; n++) {
current_process = &(service_state->interface->processes[n]);

/* Skip dead processes */
if (current_process->process_pid < 0) {
    continue;
}

/* Obtain heartbeats information */
windowed_rate = hrm_get_windowed_rate( &(current_process->heartbeats_info) );
min_rate = hrm_get_min_rate( &(current_process->heartbeats_info) );
max_rate = hrm_get_max_rate( &(current_process->heartbeats_info) );
global_rate = hrm_get_global_rate( &(current_process->heartbeats_info) );
desired_rate = (max_rate + min_rate)/2;

/* Set service specific values */
...

if (windowed_rate <= max_rate && windowed_rate >= min_rate) {
    printf("Process %d is within expectations\nm:%f w:%f M:%f, g:%f
", current_process->process_pid, min_rate, windowed_rate, max_rate, global_rate);
}
if (windowed_rate > max_rate) {
    printf("Process %d is above expectations\nm:%f w:%f M:%f, g:%f
", current_process->process_pid, min_rate, windowed_rate, max_rate, global_rate);
}
else if(windowed_rate < min_rate) {
    printf("Process %d is below expectations\nm:%f w:%f M:%f, g:%f
", current_process->process_pid, min_rate, windowed_rate, max_rate, global_rate);
}
current_process->process_pid, min_rate,
windowed_rate, max_rate, global_rate);

    } /* Service specific action on process */
    ...
    } // end of for
    */ Release lock */
    pthread_mutex_unlock(&(service_state->interface->service_interface_mutex));
    */ Has user pressed ctrl+c ? */
    if(should_quit!=0)
    {
        printf("Quitting...
");
        break;
    }
    */ If necessary, sleep before acting again */
    usleep(SERVICE_ACTION_INTERVAL);
    }
    */ Clean up and shut down */
    return_value += unregister_service(service_state);
    return_value += shut_down_service(service_state);
    if (return_value == true) {
        printf("Service has properly shutdown...
");
    }
    else {
        printf("Service cleanup not completed...
");
    }
    */ Service-specific cleanup */
    ...
    return 0;
}
4.3.3 Portability

The Services API, just like the Heartbeats API, has been compiled, run, and tested on Linux. As long as the service does not make use of any Linux-specific feature, the same code can be run also under Mac OS X.

4.4 Consensus Object

4.4.1 Functions

The Consensus Object API provides a certain number of function to handle service registration, to issue commands, and so on. Those are the most important functions:

init_consensus_object() This function initializes the data structure containing the status and creates the named pipe used for service registration.

update_list_of_monitored_apps() This function handles programs registration. It checks whether new Heartbeat-capable applications have started up or whether previously existing applications have quit. This is performed checking the files in the HEARTBEATS_ENABLED_FOLDER: in fact the Application Heartbeats create there one file per monitored application. The files are named after the PID of the process and are removed only if the application quits normally.

update_list_of_services() This function checks whether any new service has registered or unregistered. This task is carried out reading from the named pipe.
attach_consensus_object_to_service_memory() This function attaches the *Consensus Object* to the shared memory segment allocated by a service. Such segment is where commands are written.

dump_list_of_services_to_file() and read_list_of_services_from_file These functions are used to periodically save the list of services to file. This is needed since when the *Consensus Object* reads from the *named pipe* and registers a service, the only trace for the registration is in the *Consensus Object* ’s memory. If the *Consensus Object* quits or crashes, after the following start up, it will not know which services are available. For this reason it has to save the list to file and, at start up, check whether those services still exist. This procedure is not necessary for applications because their registration is not handled through a *named pipe* but through files (which are persistent).

shut_down_consensus_object() Cleanly shuts down the *Consensus Object* by deallocating resources.

enable_service(), disable_service(), release_resources_faking_heart_rate(), and make_service_overperform() Those are the possible commands that the *Consensus Object* can issue. The *Consensus Object* specifies whether a service should be enabled (first function), disabled (second function), asked for resources (third function), or asked to make the target overperform (fourth function). Since most services can target one application at a time, all these functions also require a target parameter.
get_service_state_for_process() This function can be used to check whether a service is active on a given process.

4.4.2 Consensus Object guidelines

Thanks to the provided API it is possible to easily re-implement the Consensus Object. Those are the guidelines:

• allocate a data structure of type consensus_state_t and use the given initialization function before starting
• periodically check whether new services have registered/unregistered (the API automatically attaches the segment of the new services or detaches the segment of an old service)
• periodically check whether new processes have registered/unregistered
• periodically check that registered processes and services are still functioning and have not crashed
• given the information about a process performance decide which service to enable or disable
• at the end of execution call the function to release resources

4.4.3 Portability

The Consensus Object API, just like the Heartbeats API, has been compiled, run, and tested on Linux and Mac OS X. It will work on Macs as long as services do not make use of Linux-
specific features.
In this Chapter I present the experimental results that support the validity of the use of the Application Heartbeats and the Consensus Object in a real implementation. In particular this Chapter first analyzes the overhead of the Application Heartbeats on the execution time of two example applications (Section 5.1); it then presents the results linked to the hardware/software implementation of an Implementations Library; finally some Consensus Object and Services API tests outcomes are shown.

5.1 Application Heartbeats overhead with des-fpga

In this Section the analysis of the overhead on execution time of applications adopting the Heartbeats framework is presented. Such investigation is extremely important in the context
of autonomic systems since the monitoring infrastructure must be as lightweight as possible: as already presented in 2.1.3.1 the performance gain given by the implemented self-properties cannot be nullified by the necessary monitoring architecture.

Such overhead is due to a series of system calls that the Heartbeats framework makes in order to initialize its data structures and update the global heart rate. Moreover, the overhead seems to decrease while the number of blocks increases due to the fact that the weight of the initialization tends to decrease. The initialization of the Heartbeats make use of \texttt{getpid()}, \texttt{shmget()}, and \texttt{shmat()} while the global heart rate is updated by means of two calls to \texttt{clock_gettime()} inside the \texttt{heartbeat()} call.

Two examples are presented: both execute the same code and are run under a Linux-based Operating System but while one executes on a Xilinx Virtex-II Pro FPGA board (more details can be found in Section 5.2.1) the other runs on a common x86-based computer.

The chosen application is a software implementation of the \textit{Data Encryption Standard} (from now on, \textit{DES}) cryptographic algorithm (94). While there are many other algorithms that would have served the same purpose and that would have reasonably brought to similar results, DES has a relatively simple implementation and requires the simplest possible input: text.

To benchmark the impact of the \textit{Heartbeats} on the execution time I developed a C program that implemented the DES algorithm and that could be instructed not to use the \textit{Application Heartbeats}. The application is called \texttt{des-fpga} and it starts a timer just before encrypting the first block and stops it right after encrypting the last one. The experiment has been organized this way:
• almost 30 different input sizes were tested in the range 1 to 1000 blocks.

• in order to balance slow downs and fortuities on the system the executions were repeated 10 times and then averaged

• the execution times were compared and the average percentage impact was calculated

5.1.1  **Benchmarks on a FPGA-based system**

Table I shows a sample of the data gather for the FPGA-based solution while Table II shows the averaged values that have been used to plot the graph shown in Figure 16. The average impact on the execution time is 3.52%, which is both moderate and seems to become even lower the greater the number of blocks.

5.1.2  **Benchmarks on a x86-based system**

After running the same test on an x86-based computer (Intel Core 2 Duo running at 2.4GHz, 2MB L2 cache, 4 GB RAM) an extremely puzzling result is obtained: not only the overhead is higher, but it is circa 66% of the execution time. At first glance the Heartbeats seem to have performed well in the FPGA-based scenario just because their overhead was “hidden” by a slow CPU. The truth is quite different, though. In **des-fpga** by default issues one heartbeat per encrypted block. Considering that one DES block is made of 64 bits (8 Bytes) and that on the x86 machine used for the experiment its encryption takes less that 1µs, the overhead of system calls becomes huge. This happens because system calls can be considered as a fixed cost to be
TABLE I

TABLE SHOWING A SAMPLE OF THE GATHERED DATA FOR THE FPGA-BASED EXAMPLE

<table>
<thead>
<tr>
<th>Number of encrypted blocks</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>...</th>
</tr>
</thead>
<tbody>
<tr>
<td>Execution time (µs)</td>
<td>604</td>
<td>369</td>
<td>641</td>
<td>634</td>
<td>832</td>
</tr>
<tr>
<td></td>
<td>379</td>
<td>371</td>
<td>646</td>
<td>623</td>
<td>847</td>
</tr>
<tr>
<td></td>
<td>376</td>
<td>600</td>
<td>679</td>
<td>887</td>
<td>842</td>
</tr>
<tr>
<td></td>
<td>628</td>
<td>367</td>
<td>641</td>
<td>631</td>
<td>840</td>
</tr>
<tr>
<td></td>
<td>407</td>
<td>373</td>
<td>847</td>
<td>627</td>
<td>835</td>
</tr>
<tr>
<td></td>
<td>381</td>
<td>374</td>
<td>642</td>
<td>625</td>
<td>844</td>
</tr>
<tr>
<td></td>
<td>384</td>
<td>375</td>
<td>640</td>
<td>626</td>
<td>1020</td>
</tr>
<tr>
<td></td>
<td>378</td>
<td>564</td>
<td>655</td>
<td>642</td>
<td>861</td>
</tr>
<tr>
<td></td>
<td>378</td>
<td>374</td>
<td>640</td>
<td>619</td>
<td>1050</td>
</tr>
<tr>
<td></td>
<td>384</td>
<td>376</td>
<td>667</td>
<td>623</td>
<td>836</td>
</tr>
<tr>
<td>Average (µs)</td>
<td>429.9</td>
<td>414.3</td>
<td>669.8</td>
<td>653.7</td>
<td>880.7</td>
</tr>
<tr>
<td>Impact (%)</td>
<td>3.6287</td>
<td>2.4037</td>
<td>4.7121</td>
<td>2.9785</td>
<td>...</td>
</tr>
</tbody>
</table>

paid and issuing thousands of heartbeats per second both defeats the purpose of the framework and generates a huge amount of system calls.

Obviously, since the CPU was much slower, this did not happen in the FPGA-based scenario. Taking into account these considerations it has been necessary to slightly reduce the number of heartbeats. For this experiment heartbeats were issued every 256 blocks: this quantity seems particularly reasonable since it is a multiple of the usual smallest dimension of files under Linux. The results obtained encrypting a number of blocks multiple of 256 are extremely similar to the ones obtained before. The experiment has been organized this way:

- 15 different input sizes were tested in the range 256 to 102400 blocks.
TABLE II

TABLE SHOWING THE AVERAGE EXECUTION TIME AND THE PERCENTAGE AVERAGE IMPACT ON IT CAUSED BY THE THE APPLICATION HEARTBEATS IN THE FPGA-BASED SCENARIO

| # blocks | 1  | 2  | 3  | 4  | 5  | 6  | 7  | 8  | 9  | 10 | 20 | 30 | 40 | 50 | 60 | 70 | 80 | 90 | 100 | 200 | 300 | 400 | 500 | 600 | 700 | 800 | 900 | 1000 | 10000 | 100000 |
|----------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| Average execution time (µs) | 430 | 670 | 881 | 1078 | 1251 | 1447 | 1603 | 1859 | 1967 | 2083 | 4183 | 6244 | 8591 | 11078 | 13641 | 16290 | 19065 | 22304 | 25400 | 64240 | 117.5 | 185.6 | 267.1 | 365.3 | 476.3 | 604.6 | 744.8 | 908.6 | 2688.2 | 20822.9 |
in order to balance slow downs and fortuities on the system the executions were repeated 10 times and then averaged

the execution times were compared and the average percentage impact was calculated

Sample data is shown in Table III, while final results are displayed in Table IV and Figure 17.
TABLE III

TABLE SHOWING A SAMPLE OF THE GATHERED DATA

<table>
<thead>
<tr>
<th>Number of encrypted blocks</th>
<th>256</th>
<th>512</th>
<th>768</th>
<th>1024</th>
<th>...</th>
</tr>
</thead>
<tbody>
<tr>
<td>Execution time (µs)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>189</td>
<td>180</td>
<td>352</td>
<td>340</td>
<td>513</td>
<td>675</td>
</tr>
<tr>
<td>189</td>
<td>180</td>
<td>347</td>
<td>340</td>
<td>513</td>
<td>675</td>
</tr>
<tr>
<td>234</td>
<td>277</td>
<td>364</td>
<td>364</td>
<td>514</td>
<td>675</td>
</tr>
<tr>
<td>234</td>
<td>277</td>
<td>364</td>
<td>364</td>
<td>514</td>
<td>675</td>
</tr>
<tr>
<td>191</td>
<td>180</td>
<td>352</td>
<td>380</td>
<td>516</td>
<td>693</td>
</tr>
<tr>
<td>189</td>
<td>185</td>
<td>351</td>
<td>341</td>
<td>731</td>
<td>671</td>
</tr>
<tr>
<td>222</td>
<td>180</td>
<td>399</td>
<td>343</td>
<td>511</td>
<td>674</td>
</tr>
<tr>
<td>189</td>
<td>180</td>
<td>351</td>
<td>340</td>
<td>507</td>
<td>675</td>
</tr>
<tr>
<td>188</td>
<td>198</td>
<td>356</td>
<td>341</td>
<td>516</td>
<td>832</td>
</tr>
<tr>
<td>191</td>
<td>180</td>
<td>365</td>
<td>343</td>
<td>513</td>
<td>675</td>
</tr>
<tr>
<td>Average (µs)</td>
<td>197.1</td>
<td>192.7</td>
<td>358.9</td>
<td>345.5</td>
<td>535.9</td>
</tr>
<tr>
<td>Impact (%)</td>
<td>2.2323</td>
<td>3.7336</td>
<td>4.9636</td>
<td>2.5589</td>
<td>...</td>
</tr>
</tbody>
</table>

5.2 Implementations Library: putting DES to test

In this Section we put to test the Implementations Library that has been described in Sections 3.2.8 and 4.2.

5.2.1 Environment

For the implementation a Xilinx University Program Virtex-II Pro board featuring an XC2VP30 FPGA used to provide the basic architecture and I/O ports to run and interact with the operating system. The FPGA comes with an integrated hard processor, a PowerPC 405 with a maximum frequency of 300 MHz with 16 kB of instruction cache and 16 kB of data
### TABLE IV

**TABLE SHOWING THE AVERAGE EXECUTION TIME AND THE PERCENTAGE AVERAGE IMPACT ON IT CAUSED BY THE THE APPLICATION HEARTBEATS IN THE X86-BASED SCENARIO**

<table>
<thead>
<tr>
<th># blocks</th>
<th>256</th>
<th>512</th>
<th>768</th>
<th>1024</th>
<th>2048</th>
<th>3072</th>
<th>4096</th>
</tr>
</thead>
<tbody>
<tr>
<td>Average impact (%)</td>
<td>2.2323</td>
<td>3.7336</td>
<td>4.9636</td>
<td>2.5589</td>
<td>0.9450</td>
<td>3.5766</td>
<td>4.5273</td>
</tr>
<tr>
<td>Average execution time (µs)</td>
<td>197.1</td>
<td>358.9</td>
<td>535.9</td>
<td>691.7</td>
<td>1396.7</td>
<td>2055</td>
<td>2809.6</td>
</tr>
<tr>
<td># blocks</td>
<td>5120</td>
<td>6144</td>
<td>7168</td>
<td>8192</td>
<td>9216</td>
<td>10240</td>
<td>51200</td>
</tr>
<tr>
<td>Average impact (%)</td>
<td>5.0312</td>
<td>1.2884</td>
<td>7.0051</td>
<td>6.0787</td>
<td>1.6619</td>
<td>6.6600</td>
<td>4.6812</td>
</tr>
<tr>
<td>Average execution time (µs)</td>
<td>4501.9</td>
<td>4742</td>
<td>5989.9</td>
<td>8347.1</td>
<td>6576.7</td>
<td>8168.1</td>
<td>44230.1</td>
</tr>
</tbody>
</table>
cache; the PowerPC 405 is used as the General Purpose Processor (GPP) of our embedded system and is allowed to access 256 MB of DDR-266 used as system memory through the Xilinx MPMC DDR memory controller. On top of this architecture we loaded the Linux vanilla-kernel version 2.6.33.1 and a reduced root file system built upon busybox version 1.16.0 and both the kernel and the root file system are compiled with a toolchain featuring gcc version 4.2.4 and uClibc version 0.9.30.1. When the board is powered-on the bitstream implementing the architecture is taken from the first partition of the onboard flash memory and used to program the FPGA while the kernel and the root file system are taken from the second partition of
the same flash memory. The flash memory is accessible thanks to *Xilinx SystemACE* storage controller. The console of the kernel is redirected to the *Xilinx UARTLite* serial controller and the embedded system is controller by means a *Telnet* session passing through the *Xilinx Ethernet Lite* network interface controller. The architecture contains also an IP-Core with a *Wishbone* interface implementing DES encryption and decryption capabilities and is connected to the rest of the system by means of an IP-Core implementing a *Processor Local Bus (PLB)* version 4.6 to Wishbone bridge. A schematics of the architecture is proposed in Figure 18. In a real scenario the FPGA might need to be reconfigured before having the Core DES available.

5.2.2 **Static analysis**

The first result of our work is to statically analyze the ideal behavior of the two DES implementations. We run both the implementations varying the input data size and averaging the results on tens of different trials. The results are shown in Figure 19: the hardware implementation (*square*) is faster than the software implementation (*circle*) when the input size is bigger than 50 blocks. The hardware implementation might need to be configured hence the hardware implementation considering the reconfiguration time (*triangles*) gets faster than the software implementation when the input size is bigger than 400 blocks. This analysis might seem to prove that, depending on the input size (number of blocks), it is possible to choose the best implementation statically. Yet in a dynamic scenario such execution times might change completely due to the system load or to runtime constraints (e.g., power
Figure 18. FPGA architecture
consumption): in this case a statistical approach might fail. Since many other factors cannot be predicted it is necessary to monitor the throughput of the active implementation and decide at runtime when to switch one in favor of the other.
5.2.3 A dynamic scenario

Figure 20 shows an execution scenario of the Implementations Library. Even though in \((t_0, t_1)\) the application heart rate is dropping due to the context switches we are not observing any change in the implementation because the current heart rate is still inside the desired heart rate window. In \((t_1, t_2)\) the computed heart rate exits this window for more than \(\Delta\) time instants hence the ISS decides to spend \((t_2, t_3)\) reconfiguring the FPGA and to switch to the hardware implementation. The \(\Delta\) time depends on the decision mechanism used within the system while the reconfiguration time \(R\) is spent only when the desired hardware implementation is not already configured on the FPGA. In \((t_3, t_4)\) the heart rate increases entering the desired heart rate window. The sudden decrease of the heart rate in \((t_4, t_5)\) proceeding in \((t_5, t_6)\) is caused by resources contention (e.g., buses, memory, etc.). Therefore, after waiting for \(\Delta\) time instants the software implementation is swapped in to try to increase the heart rate as it happens in \((t_6, t_7)\).

The execution scenario of the DES Implementations Library stands as an instance of enabling technology in the field of autonomic computing: the proposed methodology is valid since the information gathered at runtime enables the system to react to unpredictable and unknown conditions.

Even if at present it is not possible to guarantee in any situation a fulfillment of the Quality of Service, the Implementations Library here introduced brings a clear improvement over static implementations.
Figure 20. Application execution behavior. The interval between $m$ and $M$ defines the desired heart rate window.
5.2.4 Consensus object and Services: tests

The purpose of the following test is to quantify the overhead brought by the infrastructure designed and implemented in the previous Chapters. As we have seen throughout this dissertation, self-adaptive systems bring tremendous advantages over modern computing system. Of course we do not want this advantages to vanish because of overhead: the infrastructure must be lightweight.

We have proven in Section 5.1 that the Application Heartbeats bring a low overhead. It is now the time to analyze the Consensus Object and the Services API: we first analyze the time needed to initialize, register a service and read a command (Section 5.2.4.1); we then measure the time needed to register a process and write a command (Section 5.2.4.2); as a final step we analyze an implemented service and we compare performance of the stand-alone version against the Consensus Object - driven one (Section 5.2.4.3).

The test platform is the same one used in 5.1.2 and consists of an Intel Core 2 Duo running at 2.4GHz, 2MB L2 cache, and 4 GB RAM.

The used methodology is the same across all tests: the same experiment has been repeated ten times and then averaged.

5.2.4.1 Services tests

In this series of tests we analyze the absolute time required to initialize a service, register it to the Consensus Object , and read a command from the Consensus Object .

Initialization (Figure 21) is shown on a logarithmic scale because it takes an extremely variable
amount of time. Debugging the few lines of code that are part of the initialization function, the “culprit” immediately stands out: ftok(). ftok() is the function that has to generate a unique memory key to be used to address the shared memory segment. It takes a variable amount of time to execute and, sometimes, the initialization function has to call it repeatedly (up to a maximum of three times) before it successfully returns a valid memory key.

Registration (Figure 22) is much easier to explicate (please also note that it is not on a logarithmic scale) since the time to write to the named pipe can easily vary.

Reading a command coming from the Consensus Object (Figure 23) requires the service to obtain an exclusive lock, read the shared memory segment, release the lock, and update its
internal structures.

## 5.2.4.2 Consensus Object tests

Process registration (Figure 24) is performed by the Consensus Object, which scans for files in the \textit{HEARTBEAT\_ENABLED\_DIR}, considers only files entirely made of numbers, and adds or removes processes from its list. It is easy to see that access to the file system has a certain degree of unpredictability.

Writing a command (Figure 25) has proved very efficient and the time to perform such action is extremely stable. This happens only because the driven service only obtains the exclusive lock
when necessary and releases it right after: if the service were less efficient and less optimized (and would then keep the lock much longer) the time to write a command would dramatically increase.

The reason why writing a command requires less time than reading one is that when a service reads a command it also has to update its internal structures.

Nevertheless shared memory proves to be an extremely fast form of Inter-process communication (IPC).
5.2.4.3 Overhead on core allocator service

Summarizing the results obtained in Section 5.2.4.1 it is possible to say that the centralized autonomic infrastructure has an average overhead of 2996µs, which is almost all due to initialization. Please note that this is a non recurring allowance (it is only required at service startup) and it can be considered negligible.

We now analyze the overhead (Figure 27 and Figure 28) brought by the proposed Consensus Object - driven approach on the execution of an implemented service: the core allocator. We have developed two versions:
Figure 25. Writing a command. Ten executions, average is in red.

- a stand-alone version that automatically starts monitoring one Heartbeat-enabled application and enacts one of its policy of core allocation

- a Consensus Object - driven one that initializes, registers, and reads commands using the Services API; when enabled it enacts the same policy as the other version

The results over ten executions are quite stable and clear: the overhead on the maximum possible throughput of the service is 15.81%. Such result is encouraging since the core allocator constantly reviews and modifies its action; many other services “sleep” for a few microseconds
Figure 26. Overhead of the centralized autonomic infrastructure at service startup. Average values are shown at the top.
before starting to consider changes in their policy. This means that on many services the overhead will be much less than 15.81%.
Figure 28. Core allocator: overhead of the centralized autonomic infrastructure. Average values are shown at the top.
CONCLUSIONS AND FUTURE WORKS

“I came to the conclusion that
I am not a fiction writer.”
Tim LaHaye

In this last Chapter the obtained results are summarized and future work is planned.

The aim of this thesis has been to design and start the implementation of a system-wide self-adaptive infrastructure. Today’s systems complexity is skyrocketing and there is a strong need for transferring into modern computing systems some of the knowledge that programmers have: the aim is to reduce the burden on them and automate several optimization without requiring manual intervention.

In this context we propose an autonomic architecture capable of monitoring its current status and its progress towards certain goals, capable of performing optimizations on itself, and capable of adapting to unpredictable, unknown, and unfavorable conditions.

After stating the problem in Chapter 1 and extensively discussing the State of the Art in the field of autonomic computing in Chapter 2, we outline and throughly describe our solution in the remaining Chapters.

In Chapter 3 we present the idea behind our architecture: we aim at building a system that
observes, decides, and act. We start from the Applications Heartbeats as a simple, flexible, and lightweight monitoring facility and we have literally built around it. We have developed two novel components: the Consensus Object and the Services API. The Consensus Object is the central entity that gathers all the information coming from Heartbeats-enabled applications and decide which actions to undertake in order to make the processes reach the desired goals. The Services API is a programming interface suggested for those processes that can improve or decrease performance (either at system level and at application level): services adopting such interface are controlled by the Consensus Object which can enable them, disable them, or alter their behavior. The Services API is equal among all possible services and the Consensus Object uses the same commands to control each service. In this way new services can be added extremely easily and the whole architecture proves to be modular, flexible, and extensible. The Consensus Object and the Services API implementations have been discussed in great detail in Chapter 4.

In Chapter 5 the entire architecture is put to test. Through a series of tests it has been proved that the whole infrastructure is lightweight and ready to be expanded in the future. In particular, the Applications Heartbeats overhead has been quantified as extremely small (∼4% of the execution time). The overhead of the Consensus Object and of the Services API is also quite small: for a relatively simple service such as the core allocator the overhead is ∼15% of the execution time. Considering that most other services are either more complex (that is, they take longer to perform one iteration) or “sleep” for a few milliseconds between one iteration and the other, the average overhead will probably be even smaller. In fact the overhead on one
iteration is constant and spending more time computing one iteration reduces the proportions of the overhead.

Moreover, we have built and tested an example of Implementations Library that explores some of the concepts developed in Chapter 3 and proves the usefulness of porting an autonomic infrastructure to reconfigurable hardware.

Even if there is still a lot of work to do before the described architecture is mature, we have designed and built an enabling technology that is both novel and very promising. The developed APIs are complete and ready to be used. Future works include:

- the implementations of more services
- the design and implementation of a complex decision engine for the Consensus Object
- the extension of the Applications Heartbeats to support dynamic performance goals. This step is necessary to avoid situations like the one presented in 5.1.2; in such scenarios it is easy to imagine the performance goals (and the issued heartbeats) divided by a factor in order to bring the Applications Heartbeats overhead at a minimum
- develop a non-centralized scenario where the Consensus Object is distributed among the services instead of being central


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23. Ibm paves the way for mainstream adoption of autonomic computing.

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Historical and mathematical aspects of cryptography

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Winner of Milano Green Contest and on exhibition at Milan’s FuoriSalone 2010


2009 Preliminary Design of an Embodied, Multi-tiered Exploration of Communication among Bees, Advisors: Tom Moher.
2009  *DAGRepl enhancements, improve target support of DAGRepl, a framework for circuit optimization using replication* (Physical Design Automation, Course Project, University of Illinois at Chicago) in collaboration with Filippo Sironi, supervised by Prof. John Lillis.

2009  *Engineering of a Laboratory Simulation and Design software*, work done for Inpeco s.r.l. company.

2007–2008  *Design and implementation of the control system for an inverse pendulum (Progetto e implementazione del sistema di controllo per un pendolo inverso)*, Advisors: Sergio Bittanti and Fabio Previdi, Bachelor of Science thesis.

2008  *WI.RE: Wireless Reconfiguration using Virtex-II Pro Evaluation Board* (Progetto di Reti Logiche, Course Project, Politecnico di Milano) in collaboration with Filippo Sironi, supervised by Prof. Marco Domenico Santambrogio.

**Work Experiences**

2008–2009  **Feasibility study and software engineering**, Inpeco s.r.l., Milan, Italy.

  Reference: Marco Santambrogio, santa@csail.mit.edu, +39 335 6847022

2007–2009  **IT Consultant**, MBS s.a.s., Biella, Italy.

  Reference: Bianchetto Enrico, e.bianchetto@mbscolle.191.it, +39 335 6349984

2008  **Collaborator** for the “Big Book of Apple Hacks”, published by O’Reilly.
VITA (Continued)

Reference: Chris Seibold, cseibold@mac.com

2007 **Internship**, Domina s.r.l., Biella, Italy.

Reference: Perona Massimo, mperona@dobi.it, +39 335 7720492


Speaker at the conference “*Free Software Free Thinking*”

Skills

*Coding*  C/C++, Java, Objective-C, Cocoa, Processing, OpenGL (freglut), PHP, AppleScript, Bash scripting

*Engineering*  UML, Unit-testing, JML, JUnit

*Electronics*  Arduino, PIC

*IDEs*  Xcode, Interface Builder, NetBeans, Eclipse

*Graphics*  Photoshop, Illustrator, OmniGraffle, Aperture, Lightroom

*Office*  Microsoft Office, OpenOffice, Apple iWork (Pages, Keynote, Numbers)

*Databases*  Business intelligence, SQL

*OSes*  Mac OS X, Linux, Windows

*Soft*  sociable, detail oriented, team player, elected team leader in several university projects, used to multidisciplinary environments, good speaker (in English too)
VITA (Continued)

Additional information

Scholarships  UIC Best Student, Accenture 2008/2009

Languages  English (TOEFL: 107/120, 2008 — CAE: Grade B, 2006), Italian (mother tongue)

Interests  Music, photography, electronics

Sports  Skiing, mountain-biking